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## Flexible All-organic, All-solution Processed Thin Film Transistor Array with Ultrashort Channel

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Shrinking the device dimension has long been the pursuit of the semiconductor industry to increase the device density and operation speed. In the application of thin film transistors (TFTs), all-organic TFT arrays made by all-solution process are desired for low cost and flexible electronics. One of the greatest challenges is how to achieve ultrashort channel through a cost-effective method. In our study, ultrashort-channel devices are demonstrated by direct inkjet printing conducting polymer as source/drain and gate electrodes without any complicated substrate's pre-patterning process. By modifying the substrate's wettability, the conducting polymer's contact line is pinned during drying process which makes the channel length well-controlled. An organic TFT array of 200 devices with 2  $\mu\text{m}$  channel length is fabricated on flexible substrate through all-solution process. The simple and scalable process to fabricate high resolution organic transistor array offers a low cost approach in the development of flexible and wearable electronics.

Organic thin-film transistors (OTFTs) have attracted great attention for a wide range of applications, such as flexible displays<sup>1,2</sup>, sensors<sup>3</sup>, radio frequency identification tags<sup>4</sup>, low-cost memories<sup>5</sup>, and microprocessors<sup>6</sup>, due to their unique properties of excellent mechanical flexibility and process advantages of being compatible with high throughput and low-budget printing processes<sup>7</sup>. Over the years, significant efforts have been made to develop high carrier mobility, chemically and physically stable organic semiconductor materials with the reported device performance of OTFTs already better than that of amorphous silicon devices<sup>8–11</sup>. However, the fabrication of OTFT devices generally requires thermal evaporation through masks to deposit metal electrodes and the semiconducting layer in vacuum<sup>1–14</sup>. The process is known to be costly and difficult to scale up. To fully exploit the potentials of organic materials in the applications of low cost and flexible electronics, all-organic TFTs made by all-solution process are desired<sup>15</sup>.

Various techniques have been developed to fabricate all-organic TFTs. Conventional photolithography, which is the standard technique in inorganic semiconductor industry, has been explored for patterning polymer electrodes in bottom contact OTFTs. For example, Halik *et al.* fabricated all-organic pentacene TFTs on flexible polymeric substrates with photolithography-patterned PEDOT:PSS contacts<sup>16</sup>. A similar photo-induced patterning approach, named photochemical patterning, was developed by Gelinck *et al.* They demonstrated fully-solution-processed and all-polymer integrated circuits with conductive polyaniline electrodes defined by photochemical conversion through a shadow mask<sup>17</sup>. Although photolithography is a mature process and capable of high resolution patterning, its application in patterning OTFTs is rather limited due to degradation of organic semiconductors upon exposure of the etching chemicals.

Screen printing, which is suitable for large scale production and much cheaper than photolithography, has been exploited in OTFTs much earlier than photolithography. Garnier *et al.* demonstrated the first all-organic TFTs back in 1994 of which the source, drain and gate electrodes were all screen-printed from a graphite based ink, while the small molecular semiconductor was thermally evaporated<sup>18</sup>. Most recently, Hyun *et al.* adopted a photolithography-patterned silicon sheet as the stencil to improve the resolution of screen printing<sup>19</sup>. All-solution processed, electrolyte gated transistors were fabricated with screen-printed graphene source and drain (S/D), and an aerosol-jet printed poly(3,4-ethylenedioxythiophene) polystyrene sulfonate (PEDOT:PSS) gate. However, the obtained channel length of 90  $\mu\text{m}$  is not narrow enough to meet the demand for large drive current and high switching speed.

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Microcontact printing, which transfers patterns from stamps, can achieve a resolution as high as that of photolithography while retaining low cost. Parashkov *et al.* employed the process to pattern an electron-transfer blocking layer on a gold plate to realize selective electro-polymerization of PEDOT:PSS S/D<sup>20</sup>. The electrodes were then transferred onto a polyimide substrate where pentacene and polyvinyl alcohol (PVA) were deposited as the semiconductor and dielectric layer. The PEDOT:PSS gate was screen printed. The final channel length is as small as 10  $\mu\text{m}$ . However, the costly metal deposition in vacuum for preparing the electro-polymerization template was required to deposit the electrodes.

Another simple and cost-effective patterning technique is inkjet printing. The advantages of its fully additive feature, flexibility in pattern design, and low material consumption, make inkjet printing an attractive deposition technique in OTFT fabrication. The patterning resolution of inkjet printing, however, is generally tens of micrometers owing to the difficulty in controlling the droplet size and ink-spreading on substrates. To overcome the resolution limitation, Sirringhaus *et al.* demonstrated an approach by depositing the functional ink onto a substrate containing a predefined surface-energy pattern that was able to split the deposited ink droplets to form a narrow gap<sup>15,21</sup>. Sub-micrometer channel length was achieved by inkjet printing PEDOT:PSS ink. The process requires photolithography or electron-beam lithography to prepare the hydrophobic strips. To further exploit the direct-writing capability by inkjet printing, the authors developed a lithography-free nanopatterning technique in which a self-assembled surface layer induced the dewetting of PEDOT:PSS resulting in sub-micrometer size channels<sup>22</sup>. However, the additional surface layer sometimes causes defects at electrode contacts, and the dewetting process is time consuming.

All aforementioned techniques require multi-step procedures to pattern organic electrodes in order to achieve the narrow channel length. In our contribution, we develop a direct-writing technique to pattern organic electrodes with high resolution by inkjet printing. All-organic and all-solution processed TFT arrays were fabricated on a flexible substrate. By modifying the surface wettability of the polyethylene terephthalate (PET) surface with PVA, a channel length around 2  $\mu\text{m}$  defined by PEDOT:PSS electrodes was achieved. To the best of our knowledge, this is the highest resolution ever reported for directly inkjet-printed electrodes without any complicated substrate pre-patterning process. A 10 by 20 OTFT array were fabricated across a 3 cm  $\times$  3 cm flexible substrate showing good uniformity and high yield. Our study demonstrates that by carefully controlling ink spreading on substrates, high resolution OTFTs are achievable by direct inkjet printing.

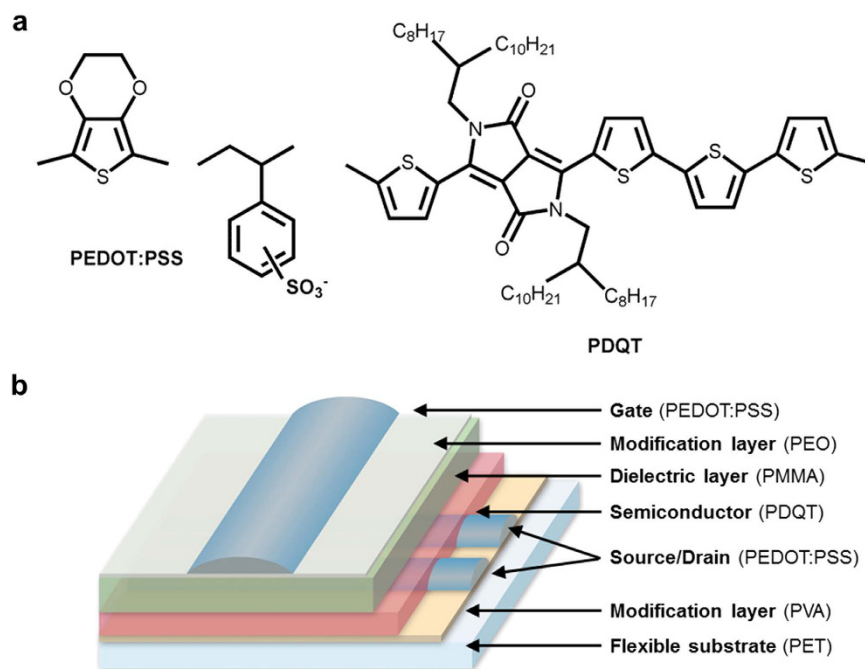
## Results

To fabricate OTFTs on a flexible substrate, a 200  $\mu\text{m}$  thick PET foil first went through 20 mins oxygen-plasma treatment to increase the surface energy. PVA in deionized water (20 mg/mL) was spin-coated onto PET film to form a 50 nm buffer layer to modify the PET surface. On top of the PVA surface modification layer, S/D electrodes were fabricated by line printing conducting polymer PEDOT:PSS<sup>23</sup>. The organic semiconductor layer was deposited by spin-coating poly [3,6-bis(40-dodecyl[2,20]bithiophenyl-5-yl)-2,5-bis(2-hexyldecyl)-2,5-dihydropyrrolo[3,4-c]pyrrole-1,4-dione] (PDQT) in chloroform (6 mg/mL). The thickness of the active layer is about 60 nm. On top of the active layer, a 500 nm film of polymethylmethacrylate (PMMA) was spin-coated from its *n*-butyl acetate solution (50 mg/mL) as the gate dielectric layer. An additional ultrathin polyethylene glycol (PEO) layer was spin-coated from a 5 mg/mL solution in methanol to modify the PMMA surface to enhance its wettability for the subsequent PEDOT:PSS gate printing, which was laid perpendicular to the S/D lines. The top-gate OTFT structure is schematically illustrated in Fig. 1. All the processes are solution-based, and all the processes are carried out in air at room temperature.

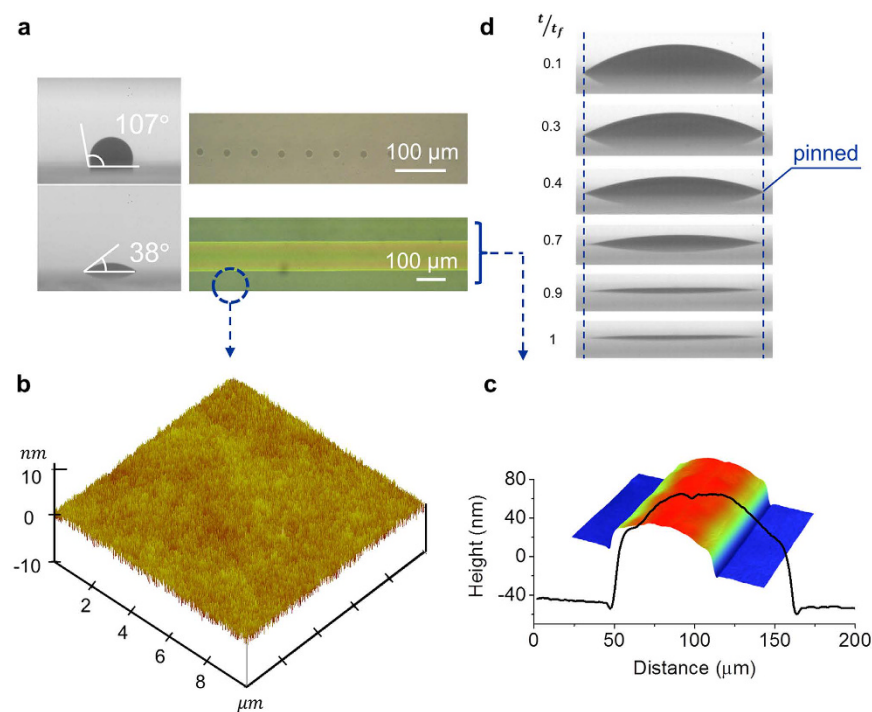
Reducing the channel length to achieve high device integration density, high operation speed and low power consumption is critical in OTFT development. Grau *et al.* reported gravure-printed OTFTs with 5  $\mu\text{m}$  channel length<sup>24</sup>. Yoshimura *et al.* achieved 4  $\mu\text{m}$  channel length by inkjet printing silver inks<sup>25</sup>. Fukuda *et al.*, taking advantage of the high-resolution off-set printing and the their customized silver ink, reached a submicrometer channel length of 0.6  $\mu\text{m}$ <sup>26</sup>. However, the studies to achieve narrow channel length all used metal inks. Though direct inkjet-printing conducting polymers (mainly PEDOT:PSS) as S/D electrodes in OTFT devices without pre-patterning substrate has been demonstrated<sup>27</sup>, the typical channel length is between 20 to 50  $\mu\text{m}$  owing to the ink spreading on substrates and droplet positioning error<sup>15</sup>. To overcome the problem, we propose here that by modifying the surface energy of the substrate with a hydrophilic and dissolvable layer, namely, the PVA buffer layer, accurate PEDOT:PSS patterns of short channels becomes possible due to the contact line pinning effect and excellent film morphology.

To obtain ultrashort channel, the S/D electrodes' patterning process is crucial. During the inkjet printing process, upon striking the substrate, the PEDOT:PSS droplet spreads first as driven by the kinetic energy. Afterwards, it shrinks because of the surface tension, and finally dries to form a stable shape<sup>28</sup>. After inkjet printing PEDOT:PSS in the polar solvent (water) on a non-polar PET surface, due to the high contact angle of 107°, the consecutively printed PEDOT:PSS droplets can't form a continuous line. Instead, isolated islands were formed, as demonstrated in Fig. 2a. To increase the hydrophilicity of the substrate, PVA was spin-coated to modify the PET surface. The atomic force microscopy (AFM) image in Fig. 2b shows the PVA has a very smooth surface with a root mean square (RMS) roughness of about 1.2 nm. On PVA, the contact angle of PEDOT:PSS was reduced to 38°. As the result, a uniform electrode line was formed by the consecutively printed PEDOT:PSS droplets as shown in Fig. 2a. The three-dimensional picture from the white light interference microscope shows a relative smooth surface of the PEDOT:PSS line free of the coffee ring (Fig. 2c). It is determined by the cross-section profile that the line width is about 106  $\mu\text{m}$ , and the line thickness is about 100 nm.

A common practice to increase the substrate surface energy is UV/ozone or oxygen plasma treatment<sup>29,30</sup>. However, the wettability provided by the treatments is not stable in air<sup>31,32</sup>. With PVA film, the hydrophilic layer guarantees the uniform coating of the PEDOT:PSS electrode array across a large area during the printing process without any wettability deterioration.

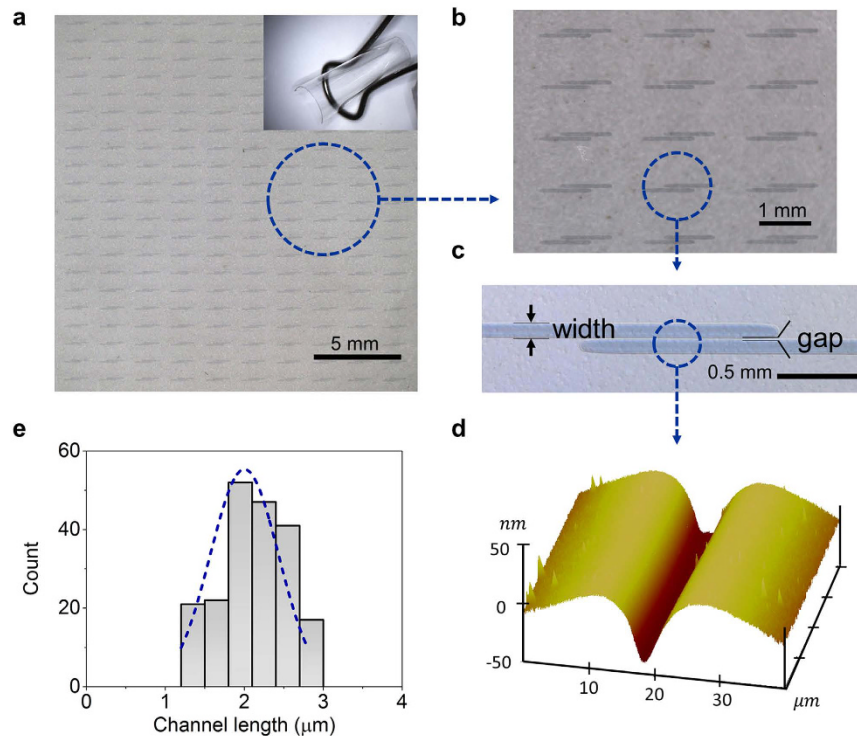


**Figure 1. OTFT structure.** (a) Molecular structures of PEDOT:PSS and PDQT. (b) Device structure of all-organic, all-solution processed OTFTs with inkjet-printed PEDOT:PSS electrodes on flexible substrate.



**Figure 2. Printed PEDOT:PSS lines on the PVA modified substrate.** (a) The contact angle of PEDOT:PSS on PET surface is 107°. Line printing on the substrate results in isolated islands due to the small solid surface energy. After PVA modification, PEDOT:PSS contact angle becomes 38°. A continuous line is formed by line printing PEDOT:PSS. (b) The AFM picture shows the smooth PVA-modified PET surface. (c) The three-dimensional white light interference microscopic picture and the cross-sectional profile of the printed PEDOT:PSS line on PVA-modified PET. (d) Sequential picture of PEDOT:PSS drying process on PVA-modified PET.  $t_f$  is the total drying time.

Moreover, the PVA layer can pin the contact line during PEDOT:PSS film formation. Figure 2d shows the drying process of the PEDOT:PSS droplets on PVA/PET substrate. The total drying time  $t_f$  was 600 s. The contact



**Figure 3. A printed array of ultrashort channels.** (a) The printed PEDOT:PSS array ( $10 \times 20$ ) on the flexible PET substrate with  $2 \mu\text{m}$  channel length. Inset: Picture of the array on the  $3 \text{ cm} \times 3 \text{ cm}$  substrate, bended with bending radius of about  $5 \text{ mm}$ . (b) The zoom-in picture of the array. (c) Picture of a single channel. (d) AFM picture of the PEDOT:PSS lines with a gap of about  $2 \mu\text{m}$ . (e) The channel-length distribution of the 200 channels.

line (as denoted by the dash lines) was strongly pinned due to the dissolution of PVA underneath the PEDOT:PSS droplet, and the contact line did not withdraw during the whole drying process as shown at  $t = 60 \text{ s}$ ,  $180 \text{ s}$ ,  $240 \text{ s}$ ,  $420 \text{ s}$ ,  $540 \text{ s}$ , and  $600 \text{ s}$ , respectively. In our early work, PEDOT:PSS was inkjet printed on an oxygen-plasma treated PMMA surface<sup>33</sup>. The contact line withdrew during PEDOT:PSS solvent evaporation due to insufficient interfacial adhesion. The minimum channel length of  $20 \mu\text{m}$  was determined by the receding radius of the droplet. In the current study, with surface modification of the hydrophilic PVA, precise control of the line gap and line width can be realized owing to the pinned contact line<sup>34</sup>. As the result, an ultrashort channel was achieved by direct inkjet printing through careful calculation and adjustment of the printing parameters without any complicated substrate pre-patterning process.

The stable line width through inkjet printing can be estimated by the following equation (1):<sup>35</sup>

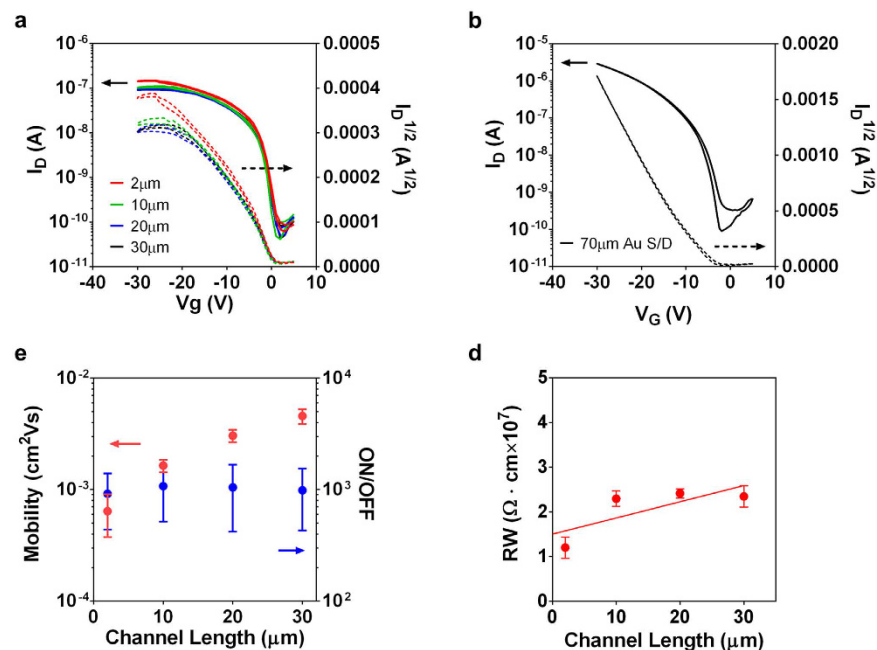
$$w = \sqrt{\frac{2\pi d_0^2}{3p\left(\frac{\theta}{\sin^2 \theta} - \frac{\cos \theta}{\sin \theta}\right)}} \quad (1)$$

where  $d_0$  is the droplet diameter in air,  $\theta$  is the contact angle of the droplet on the substrate, and  $p$  is the distance between the consecutive droplets' landing positions. It's clear that for a given printer cartridge with the fixed  $d_0$  and  $\theta$ ,  $p$  is the only parameter which can be optimized. To obtain fine lines with smooth edges,  $p$  can neither be too small nor too large<sup>36</sup>. The optimized  $p$  for PEDOT:PSS jetted from a  $30 \mu\text{m}$  nozzle, is around  $50 \mu\text{m}$ , which resulted in a line width of about  $106 \mu\text{m}$  without coffee ring (Fig. 2c). Thus, by carefully adjusting the line-center distance between two PEDOT:PSS electrodes, the printed channel length can be determined.

By setting the printing line-center distance at  $118 \mu\text{m}$  after taking into account the line width and the substrate positioning accuracy, a  $2 \mu\text{m}$  channel length was successfully achieved. To test the printing uniformity, 200 structures were fabricated on a  $3 \text{ cm} \times 3 \text{ cm}$  PET substrate with  $2 \mu\text{m}$  channel length and  $970 \mu\text{m}$  channel width. The microscopic pictures and AFM topography of the printed channels are shown in Fig. 3. The inset of Fig. 3a shows that the all-solution processed OTFTs are flexible and transparent. The channel length distribution in Fig. 3e shows the direct inkjet printing is reliable and repeatable ( $2.0 \mu\text{m}$  in average with  $0.5 \mu\text{m}$  standard deviation). No shortening of the S/D electrodes was detected confirming the high yield of the device array fabrication.

The typical transfer characteristics of the  $2 \mu\text{m}$  channel OTFT device is shown in Fig. 4a. The maximum hole mobility is  $(0.64 \pm 0.27) \times 10^{-3} \text{ cm}^2/\text{V} \cdot \text{s}$ , ON/OFF ratio is  $(0.92 \pm 0.48) \times 10^3$ , and turn-on voltage is  $1.94 \pm 0.32 \text{ V}$ . The statistics is calculated from a total of 96 devices. For comparison, OTFTs with thermal-evaporated Au S/D electrodes and a channel length of  $70 \mu\text{m}$  ( $W/L = 4$ ) were fabricated. As shown in Fig. 4b, the devices exhibit a





**Figure 4. Performance of OTFTs.** (a) Transfer characteristics of the devices with printed PEDOT:PSS electrodes. The channel width is fixed at 970  $\mu\text{m}$ , and channel lengths are 2  $\mu\text{m}$ , 10  $\mu\text{m}$ , 20  $\mu\text{m}$  and 30  $\mu\text{m}$ , respectively. (b) Transfer characteristic of the device with evaporated Au S/D electrodes. W/L = 280  $\mu\text{m}$ /70  $\mu\text{m}$ . (c) The dependence of mobility and ON/OFF ratio on the channel length. (d) The channel resistance of devices with different channel lengths (normalized to the channel width). The intercept at the y axis indicates the contact resistance.

Channel length ( $\mu\text{m}$ )	Mobility (cm <sup>2</sup> /V s)	ON/OFF	$V_{\text{turn-on}}$ (V)
2	$(0.64 \pm 0.27) \times 10^{-3}$	$(0.92 \pm 0.48) \times 10^3$	$1.94 \pm 0.32$
10	$(1.64 \pm 0.21) \times 10^{-3}$	$(1.07 \pm 0.56) \times 10^3$	$1.93 \pm 0.32$
20	$(3.05 \pm 0.39) \times 10^{-3}$	$(1.05 \pm 0.62) \times 10^3$	$1.83 \pm 0.13$
30	$(4.57 \pm 0.70) \times 10^{-3}$	$(0.98 \pm 0.55) \times 10^3$	$1.59 \pm 0.50$

**Table 1. Summary of the OTFT performance at different channel length.**

mobility of 0.55 cm<sup>2</sup>/V·s, an ON/OFF ratio of  $3.26 \times 10^4$ , and a turn-on voltage at  $-5.5$  V. A previous study on non-annealed PDQT devices using a bottom-gate top-contact structure with evaporated Au electrodes reported a mobility of 0.89 cm<sup>2</sup>/V·s, an ON/OFF ratio of  $10^7$ , and a threshold voltage of  $-2.6$  V<sup>37</sup>. The performances of the Au devices indicate that the poor performance of the 2- $\mu\text{m}$ -channel all-organic devices is not due to the semiconducting or the dielectric layer, but the printed PEDOT:PSS electrodes and their interface to the semiconducting layer. It is observed that while the OFF current of 2- $\mu\text{m}$ -channel devices remains at the same level as the Au device, the ON current is about an order of magnitude smaller, which directly reduces the ON/OFF ratio to about  $10^3$ . It is also noticed that the slope of the square root of drain current quickly decreases as the gate voltage increases. Therefore, it is speculated that the poor performance may be the result of the relatively large contact resistance at the printed electrodes.

To confirm it, all-solution processed, all-organic TFTs with channel lengths of 30  $\mu\text{m}$ , 20  $\mu\text{m}$  and 10  $\mu\text{m}$  were fabricated. The channel width was kept at 970  $\mu\text{m}$ . Transfer characteristics are presented in Fig. 4a,c gives the statistical distribution of the mobility and ON/OFF ratio for the devices with different channel length. The statistics are calculated from a total of 46, 93, and 61 devices, for 30  $\mu\text{m}$ , 20  $\mu\text{m}$ , and 10  $\mu\text{m}$  channel length, respectively. The device performances are summarized in Table 1. The mobility shows a decreasing trend as the channel length shortens (Fig. 4c), which is consistent with the effect of large contact resistance in TFTs<sup>38</sup>. By further plotting the channel resistance (normalized to the channel width) as a function of channel length in Fig. 4d (at the gate voltage of  $-5$  V), a contact resistance of about 15.0 M $\Omega \cdot \text{cm}$  is identified. The value is about two to three orders of magnitude larger than typical solution-processed OTFTs<sup>39</sup>. Such large contact resistance will limit the drain current at high gate voltages. As a result, calculating mobility from typical equations will lead to much smaller values<sup>38</sup>. The origin of the large contact resistance could come from the relatively large injection barrier from PEDOT:PSS electrodes to the semiconducting PDQT, as well as the large bulk resistance of intrinsic PEDOT:PSS<sup>15</sup>. Further device optimization by, for example, inserting an injection layer between PEDOT:PSS electrodes and the PDQT layer, or raising the conductivity of PEDOT:PSS, will improve the device performance<sup>40,41</sup>.

In summary, all-organic and all-solution processed OTFT array is fabricated on flexible substrate PET. The source, drain and gate electrodes are deposited by inkjet printing conducting polymer PEDOT:PSS. By modifying the PET substrate with water-dissolvable PVA, the contact line of PEDOT:PSS is pinned during the drying process resulting in well-defined edges. The pinning effect subsequently enables precise control of the printed channel length. Ultrashort channel of 2  $\mu\text{m}$  is successfully realized without any complicated substrate's pre-patterning process. No short circuiting was found by printing a large array with 200 electrode pairs. Due to the large contact resistance, the printed devices show inferior performances to those with evaporated metal electrodes. By further reducing the linewidth of the printed patterns as well as optimizing the device performance, application of all-printed, high resolution TFTs in flexible displays, sensor chips, and radio-frequency identification tags will become practical.

## Methods

**Materials.** All the chemicals and materials were purchased and used as received unless otherwise noted. PEDOT:PSS Clevios PH 500 was purchased from Heraeus Deutschland GmbH & Co. KG. PVA (98–99% hydrolyzed) was purchased from Alfa Aesar. To make PVA solution (20 mg/mL) in deionized water, the solution was heated to 90 °C for 5 hours. PDQT (molecular weight >30,000) was purchased from Lumtec Corp. PMMA, PEO, and all the solvents were purchased from Aldrich, Inc. All the solutions were filtered through a 0.45  $\mu\text{m}$  filter before use.

**Device Fabrication and Characterization.** PEDOT:PSS printing was carried out on an inkjet printer (Jetlab II) from Microfab Technologies, Inc. The printer head's orifice diameter is 30  $\mu\text{m}$ . Film thickness and surface morphologies were obtained from a Dektak 150 surface profiler (Veeco Instruments, Inc.). The three-dimensional white light interference microscopy pictures were recorded by Veeco Instruments' NT 9300 surface profiler. The optical microscopic images were taken with a Nikon Eclipse E600 POL with a DXM1200F digital camera. The transistor characteristics of the devices were measured using an Agilent 4155C semiconductor parameter analyzer connected to a Cascade manual probe station.

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## Author Contributions

J.W., J.P. and W.X. designed the research; W.X. and Z.H. carried out the experiments; W.X., Z.H. and L.L. analyzed the data; H.L., L.L. and Y.C. provided valuable discussion and suggestion; W.X., Z.H. and J.W. wrote the paper.

## Additional Information

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