

# **OPEN** Control and Manipulation of Nano **Cracks Mimicking Optical Wave**

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Generally, a fracture is considered as an uncontrollable thus useless phenomenon due to its highly random nature. The aim of this study is to investigate highly ordered cracks such as oscillatory cracks and to manipulate via elaborate control of mechanical properties of the cracking medium including thickness, geometry, and elastic mismatch. Specific thin film with micro-sized notches was fabricated on a silicon based substrate in order to controllably generate self-propagating cracks in large area. Interestingly, various nano-cracks behaved similar to optical wave including refraction, total internal reflection and evanescent wave. This novel phenomena of controlled cracking was used to fabricate sophisticated nano/micro patterns in large area which cannot be obtained even with conventional nanofabrication methods. We also have showed that the cracks are directly implementable into a nano/micro-channel application since the cracks naturally have a form of channel-like shape.

There is wide interest in development of nano-patterning techniques based on a silicon substrate intended for interdisciplinary studies including nano-fluidics<sup>1</sup>, nano-electronics<sup>2</sup> and nanoelectromechanical systems (NEMS)<sup>3</sup>. Various nano-patterning techniques utilizing lithography as well as non-lithographic approaches are available nowadays4. Several frequently used and nanoscale precision assured techniques include electron beam lithography (EBL), scanning probe microscopy (SPM) or scanning tunneling microscopy (STM), and atomic force microscopy (AFM)<sup>5-8</sup>. The resolution of these techniques are highly precise, however the fabrication processes are often time consuming and expensive. In addition, serial processes are required for large area patterns because a single process area of these techniques is usually smaller than 1 cm × 1 cm. On the other hand, nanoimprinting, self-assembly, and block-copolymer lithography techniques have attained patterning capabilities equivalent or even better than in some respect to photolithography at relatively lower cost<sup>9–12</sup>. These techniques are typically carried out under non-vacuum and room temperature environment which leads to reduced cost and processing steps. Nevertheless, there are limitations of these techniques: resultant patterns are simple repetitive types, and fabrication of a master template which often involves the aid of state of art optical lithography technology is required. Foremost, both techniques are hindered by the difficulties of defect free fabrication over large area. For these reasons, large area nanopatterning is a challenging assignment.

Since discovery of cracking control in glass material by Yuse and Sano<sup>13</sup>, various research have introduced possibility of the cracking as a patterning method and have found applications especially in micro and nanofluidics<sup>14,15</sup> and electronics<sup>16,17</sup>. In turn, awareness of using cracking as a viable candidate for nanopatterning technique have steadily emerged from being a totally random phenomenon<sup>15,18–22</sup>. Recently, the prospect of controlled nanocrack as a nanopatterning method has been revitalized by Koo et al.<sup>23</sup> Using chemical vapor deposition (CVD) of glassy material on a silicon substrate, precise control of crack initiation and stop of different propagation modes of fracture such as straight and oscillatory crack have been successfully demonstrated. The cracks in Koo's report showed great nanoscale features including extremely good line edge roughness (LER), line width roughness (LWR), and aspect ratio of width to depth. Another similar attempt has been made by Kim et al.24 by utilizing differential thermal expansion of glassy films on a metal patterned silicon substrate. Although there have been some instabilities in the oscillation of the

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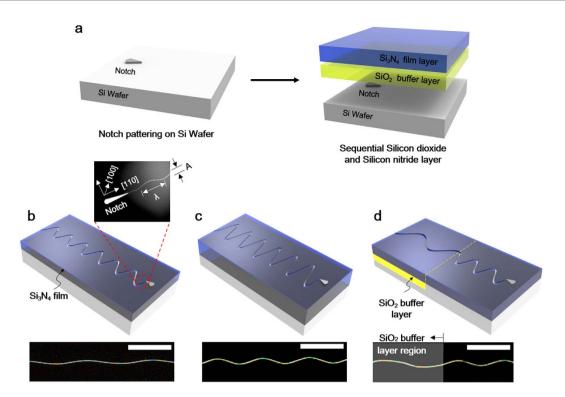


Figure 1. Schematic diagram of crack generation and control. (a) Structure used for nanocrack generation. (b-d) An oscillatory crack generated on  $Si_3N_4$  film with propagation direction in [110] on (100) silicon wafer and its corresponding optical microscopic image. Inset scale is  $100\,\mu m$ . Notice that the wavelength and amplitude of the oscillatory crack can be manipulated (c) by changing  $Si_3N_4$  layer thickness and by (d) introducing  $SiO_2$  buffer layer.

crack, control of wave parameters of the oscillatory crack has been successfully demonstrated in microscale. Those studies showed remarkable control of naturally propagating oscillatory cracks as a mean of nanopatterning. In an attempt to expand this prospective phenomenon even further for various applications including nano/micro channel fabrication, we demonstrate elaborate control of the oscillatory cracks including initiation, stop, and manipulation of wave properties at a specific location or within a region of interest while maintaining high aspect ratio, low LER, and LWR of the cracks, using CVD deposition of glassy films on a silicon. In addition, we find that the oscillatory cracks fabricated in this manner exhibit optical wave-like behaviors such as refraction, total internal reflection and evanescent wave. Use of these unique optical wave like characteristics of the oscillatory crack enables various nanopatterns which are directly applicable to nanochannel fabrication.

# **Results and Discussions**

In order to generate an oscillatory crack, a cracking medium composed of Si<sub>3</sub>N<sub>4</sub> or Si<sub>3</sub>N<sub>0</sub> and SiO<sub>2</sub> is deposited on a Si substrate as illustrated in Fig. 1a. In such system, residual stress after deposition process induces tensile stress in the thin film<sup>25</sup>. When the thin film is deposited on a notch structure, either positive or negative pattern shape, an acute stress concentration is induced at the notch tip. The tip also provides an initial flaw for a fracture to grow upon. As a result, a channeling crack is initiated to release the stored elastic energy in the Si<sub>3</sub>N<sub>4</sub> thin film at the tip as shown in Fig. 1b. The cracks initiated in this fashion self-propagate in steady oscillatory manner in [110] direction on (100) silicon substrate until the point where another stress concentration that surmounts the driving force is reached<sup>26</sup>. Although the exact cause of the oscillation is not clearly defined yet, it appears to be related to the crystal structure of underlying silicon substrate. Under identical experimental condition, the oscillatory cracks have not been discovered on (111) oriented Si substrates. On (110) oriented Si substrates, oscillatory cracks have been observed, however straight cracks predominantly occurs unlike the (100) Si substrate case. (See supplementary information Fig. S2) The simplest method for manipulation of the oscillatory cracks in Si<sub>3</sub>N<sub>4</sub>/Si film-substrate system is to regulate Si<sub>3</sub>N<sub>4</sub> deposition thickness which influences the amplitude of oscillatory cracks (Fig. 1c). Figure 1b,c shows increased Si<sub>3</sub>N<sub>4</sub> film thickness results in substantial amplitude increase of the oscillatory cracks. Simultaneously, the wavelength of the oscillatory crack varies with the film thickness. In order to manipulate the wavelength of the oscillatory crack separately, a SiO<sub>2</sub> buffer layer is introduced between Si substrate and Si<sub>3</sub>N<sub>4</sub> thin film. As shown in Fig. 1d, the wavelength of oscillatory crack is considerably increased by the buffer layer with negligible amplitude difference.

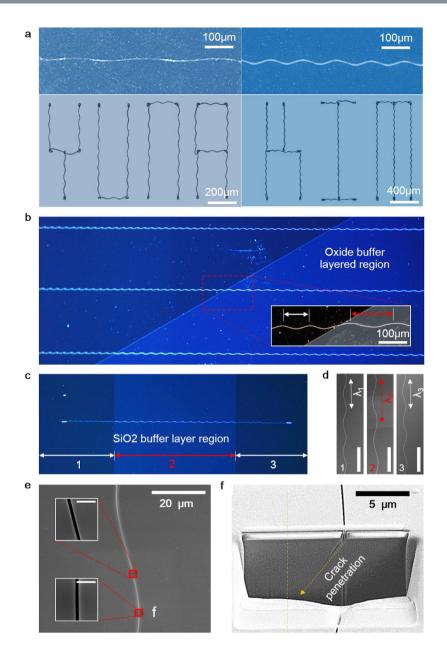


Figure 2. Wave property control of sinusoidal nanocrack. (a) (top) Oscillatory cracks with different amplitude and wavelength using different  $Si_3N_4$  film deposition thickness and underlying buffer oxide layer. (bottom) Letters ("YUNA KIM") drawn by crack control. (b) Wavelength modulation with oxide buffer layer. Note the high aspect ratio nanopatterns each of which having nanoscale width and centimeter scale length. (c,d) An oscillatory crack is initiated and stopped with modulated wavelength at the center with buffer layer. Note that the wavelength of the oscillatory cracks are reversible when it passes through different regions. The wavelength is significantly increased in the silicon dioxide buffer layer region, and corresponding SEM images of the region 1, 2, and 3 are shown. The inset scale bar is  $100 \,\mu\text{m}$ . (e) Width of a nanocrack in a typical oscillatory crack. The inset scale bar is  $1 \,\mu\text{m}$ . (f) Cross sectional image of an oscillatory crack in depth direction obtained via FIB. Note that the tip of cross-sectional crack profile was not vertical but it points to the center line of in-plane propagation direction of the oscillatory crack.

Complementary introduction of the oxide buffer layer between the  $Si_3N_4$  film and Si wafer enables manipulation of both wavelength and amplitude of the oscillatory crack. Top left image of Fig. 2a shows an oscillatory crack generated on  $900\,\mathrm{nm}$   $Si_3N_4$  film with  $50\,\mathrm{nm}$   $SiO_2$  buffer layer. The wavelength is  $\sim\!200\,\mu\mathrm{m}$  and the amplitude is roughly 1/20 of the wavelength. On the contrary, the wavelength of oscillatory cracks found on  $1.2\,\mu\mathrm{m}$   $Si_3N_4$  film without oxide buffer layer is  $\sim\!100\,\mu\mathrm{m}$  and the amplitude is roughly 1/10 of the wavelength as shown in Fig. 2a (top right). Therefore, combinatory use of specific  $Si_3N_4$  film thickness that corresponds to target amplitude and the  $SiO_2$  buffer layer provides a utility

for wavelength and the amplitude control. Combined with crack notch and stop design, various arbitrary patterns such as letters (*YUNA KIM*) with various oscillatory crack wavelength were demonstrated (Fig. 2a).

Selectively etched SiO<sub>2</sub> patterns prior to Si<sub>3</sub>N<sub>4</sub> film deposition enables localized control of wave characteristics of the oscillatory crack as shown in Fig. 2b,c. When an oscillatory crack propagates over the SiO<sub>2</sub> buffer layer region along in-plane direction, the wavelength of the oscillatory crack increases at the interface of two regions while amplitude remains unchanged. In Fig. 2b, multiple parallel oscillatory cracks are generated by placing a notch array in parallel manner. Inset of Fig. 2b confirms noticeably increased wavelength of an oscillatory crack on the oxide buffer layer. Unless there are special constraints such as defects or other structures including crack stop or notches, the cracks propagate continuously in the buffer layer region in the same way as they do in non-buffer region. The wavelength of the oscillatory crack is dependent on the underlying layer. As shown in Fig. 2c, an oscillatory crack initiated from a notch has initially shorter wavelength (region "1"). Once the crack enters the oxide buffer layers (region "2"), the wavelength becomes elongated. When it leaves the oxide buffered layer region and reaches non-buffer layer region (region "3"), the increased wavelength comes back to its initial wavelength. This wavelength shifting is totally reversible regardless of the shape of underlying buffer layer. Corresponding SEM images of the oscillatory crack in each regions confirm locally increased wavelength of the oscillatory crack in the buffer layer region, shown in Fig. 2d. The oxide buffer layer has little effect on width of the crack channel. Figure 2e shows the width of a typical oscillatory crack which is approximately 120~150 nm. Although there are slight variations depending on the location or geometry of structures near the cracks, the width of the oscillatory cracks in oxide buffer region is not considerably different from that of non-buffer region. In addition, the width of the oscillatory crack has no direct relationship with other wave parameters such as wavelength and amplitude (see supplementary information Fig. S3).

In any cases of the oscillatory cracks, significant substrate crack penetration through the depth direction was observed. This penetration of the crack into the Si substrate is always much deeper than the thickness of the  $Si_3N_4$  thin film due to high elastic energy of the  $Si_3N_4$  film<sup>27</sup>. It was revealed that the tip of cross-sectional crack profile was not vertical but it points to the center line of in-plane propagation direction of the oscillatory crack. The maximum penetration angle occurs at the peaks of the wave and the corresponding angle coincides with (111) plane of the Si (100) substrate as revealed in FIB (focused ion beam) cross section image shown in the Fig. 2e. As the oscillatory crack completes one cycle of sine wave, the penetration angle rotates from approximately  $55^{\circ}$  to  $-55^{\circ}$  degrees with respect to center of axis which coincides with in-plane propagation direction of the oscillatory crack. This substrate penetration is closely related to the cause of oscillatory cracks. The surface energy of crystal silicon's cleavage plane varies with number of broken bonds. The number ratio is known to be  $1:(3/2)^{(1/2)}:3^{(1/2)}$  for  $\{111\}$ ,  $\{110\}$ , and  $\{100\}$  respectively for Si  $\{100\}^{28}$ . The alteration of penetration angle between one  $\{111\}$  plane to another  $\{111\}$  requires fracture in higher surface energy plane inevitably, thus the oscillatory cracks are preferred under higher stress state than the straight cracks assuming that there are no defects.

In a typical thin film/substrate system with an assumption that the film and substrate are elastically homogeneous and the substrate is substantially thicker than the film, the energy release rate for initiation,  $G_{initiation}$  and steady state propagation of a channeling crack,  $G_{ss}$  are

$$G_{initiation} = Y \frac{\sigma^2 a}{\overline{E}} \tag{1}$$

$$G_{ss} = Z \frac{\sigma^2 h}{\overline{E}} \tag{2}$$

where  $\overline{E}$ , h, and a are plane strain tensile modulus, thickness of the thin film, and initial flaw size, respectively and  $\sigma^2$ , Y and Z are uniform tensile loading due to residual stress, dimensionless quantity for crack initiation and steady state propagation, respectively<sup>27,29</sup>. In the Si<sub>3</sub>N<sub>4</sub>/Si film-substrate system, crack initiation rate of the oscillatory crack significantly increases as the film thickness h increases since the thicker Si<sub>3</sub>N<sub>4</sub> film thickness results in higher strain energy in the system<sup>27</sup>. All cracks including random and oscillatory form are rarely found when h is less than 900 nm because insufficient amount of residual stress for the initiation is established whereas initiation rate is greatly improved where h is increased to near 1.1  $\mu$ m. From those results, it is conjectured that increased Si<sub>3</sub>N<sub>4</sub> film thickness leads to higher residual tension  $\sigma^2$  in the given system. Similar to crack initiation rate, amplitude of oscillatory crack is related to Si<sub>3</sub>N<sub>4</sub> film thickness. As shown in Fig. 3a, the amplitude of the oscillatory crack has a proportional relationship with the Si<sub>3</sub>N<sub>4</sub> film thickness although there are some uncertainties due to unintended structures such as defects.

In order to independently control the wavelength from the amplitude in the oscillatory crack and to have the film thickness fixed at optimized value for highest oscillatory crack initiation, additional thin film layer is employed under  $\mathrm{Si}_3\mathrm{N}_4$  film.  $\mathrm{SiO}_2$  is an ideal candidate due to its compatibility with micro fabrication process and no significant total film thickness increase. The  $\mathrm{SiO}_2$  buffer layer promotes favorable effect on the given system with relatively small increase in the overall thickness of the  $\mathrm{Si}_3\mathrm{N}_4/\mathrm{SiO}_2$ 

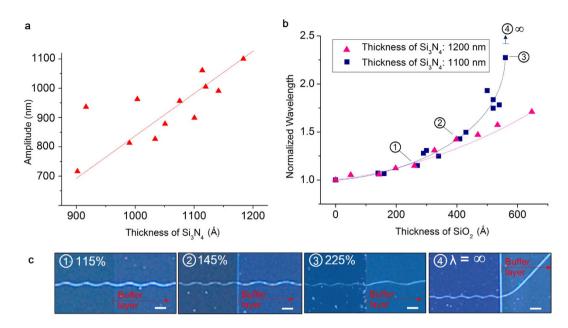


Figure 3. Wavelength and amplitude control of sinusoidal nanocrack. (a) Normalized wavelength and amplitude of the oscillatory cracks plotted against different  $Si_3N_4$  deposition thicknesses. (b) Normalized wavelength of the oscillatory cracks plotted against  $SiO_2$  buffer layer thickness and (c) corresponding optical microscope images of oscillatory cracks. Scale bar is  $100 \, \mu m$ .

multi-layered thin film. The range of this additional layer thickness required for wavelength manipulation is up to 1000 Å which is relatively small as compared to  $1.1\,\mu\text{m}$  of  $Si_3N_4$  film thickness at optimal initiation rate. As shown in Fig. 3b, the  $SiO_2$  buffer layer thickness under  $700\,\text{Å}$  is sufficient to convey wavelength increase up to 225% while the amplitude change in the oscillatory crack, which is primarily governed by the film thickness, is restrained. The wavelength of the oscillatory cracks has exponential relationship with the buffer layer thickness as shown in Fig. 3b. When the  $SiO_2$  buffer layer thickness is larger than  $700\,\text{Å}$  for  $1.2\,\mu\text{m}$  of  $Si_3N_4$  film, oscillatory crack propagation mode dramatically changes into straight mode. When the  $Si_3N_4$  film thickness is reduced, wavelength increase rate per unit  $SiO_2$  buffer layer thickness is also increased. The result shown in Fig. 3b,c indicates that the wavelength of the oscillatory crack increase rate surges with reduced  $Si_3N_4$  film thickness.

Interestingly, the oscillatory crack passing through different buffer layer regions shows a unique behavior similar to that of light. Complementary use of various SiO<sub>2</sub> buffer layer patterns and wavelength manipulation enables fabrication of sophisticated nano-patterns. As shown in Fig. 4a, wavelength of an incident oscillatory crack wave in the SiO<sub>2</sub> buffer layer region becomes elongated as if the wavelength of an optical wave increases when it propagates through a medium with lower refractive index. Bending of an oscillatory crack's propagation direction was also observed just like the refraction optical wave. As shown in Fig. 4b, the propagation direction and mode were changed when passing through SiO<sub>2</sub> buffer layer. The buffer layer with thickness greater than 70 nm turns the crack's propagation mode from oscillatory to straight. At in-plane interface of SiO<sub>2</sub> buffer layer, the incident oscillatory crack kinks to conform to straight crack's principle propagation direction. As a result, the in-plane propagation direction is changed from [110] to [100], which are the principle propagation directions of the each modes on (100) Si substrate, respectively. When the straight crack leaves the buffer oxide region, exactly opposite phenomenon occurs. As shown in the cross section image (Fig. 4c) attained using FIB, the straight crack in the SiO<sub>2</sub> region (upper image) has crack penetration angle similar to that of oscillatory crack at the incident phase (lower image). This indicates the incident crack penetration angle is maintained throughout the propagation in the SiO<sub>2</sub> buffer layer region, thus the phase of incident wave at the interface determines the in-plane kinking direction.

At an arbitrary phase of the oscillatory crack, corresponding penetration angle with respect to the principle propagation axis of the oscillatory crack always lies between  $-55^{\circ}$  and  $0^{\circ}$  or  $+55^{\circ}$  and  $0^{\circ}$ . For simplicity, positive phase which is right hand side with respect to the principle propagation axis of the oscillatory crack from the view point towards crack tip is defined as right hand phase (RHP) and the opposite is defined as left hand phase (LHP). When the phase is in RHP range, the penetration angle is also in the positive range. In such case, the crack kinks to the left at the interface, and vice versa as shown in Fig. 4d. Similarly, when the crack exits the buffer layer region, the straight crack turns into the oscillatory crack. The straight crack's penetration angle does not exceed its maximum angle of  $55^{\circ}$ , and the penetration angle of the incident phase is maintained, therefore when the straight crack exits the buffer region, it kinks to the opposite direction of the initial kinking direction at the incident where

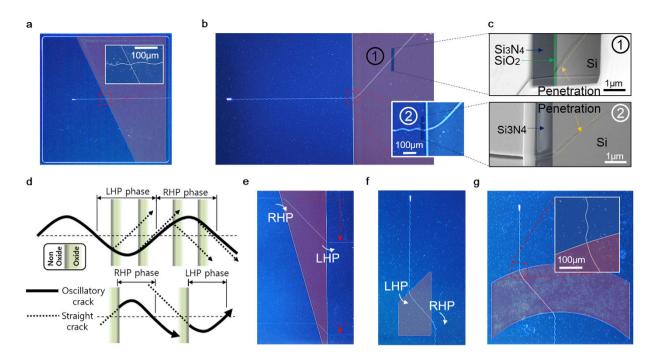


Figure 4. Optical wave-like behavior of nanocracks and their direction control. (a) Wavelength modulation when it pass through thin SiO<sub>2</sub> buffer layer. (b) Propagation mode changed from oscillatory mode to straight mode by thick SiO<sub>2</sub> buffer layer and (c) cross sectional (FIB) image of straight (top) and oscillatory (bottom) crack. (d) Selection of crack kinking direction when oscillatory crack impinging into SiO<sub>2</sub> buffer layer region (top) and when straight crack exits the SiO<sub>2</sub> buffer oxide region (bottom). (e) Propagation phase shifted by straight crack travel distance. (f) Propagation phase shifted leftward direction using SiO<sub>2</sub> buffer layer. (g) Propagation direction manipulated by SiO<sub>2</sub> buffer layer patterned like an optical lens.

transition occurred from oscillatory to straight mode. In order to successfully control kinking direction, wavelength control is crucial in terms of anticipating the exact path of the propagation and of realizing elaborate crack patterning.

Using these optical wave-like characteristics of the oscillatory crack with elaborate control of incident phase and angle at in-plane interface of  $SiO_2$  buffer layer, precise manipulation of nano crack can be realized, including propagation shift. Figure 4e shows selective shift of the two waves by adjusting propagation length of straight crack portion. The propagation shift in opposite direction can also be attained as shown in Fig. 4f. When the incident angle between the oscillatory wave and the interface is properly controlled, the cracks can be focused into a specific location as if the optical wave is focused by lens as shown in Fig. 4g.

Other optical wave-like characteristics of the oscillatory crack include amplification, total internal reflection, and evanescent wave. Using these unique optical wave like characteristics of the oscillatory crack enables sophisticated manipulation of nano-cracks for the fabrication of arbitrary nano-patterns over large area. By placing a triangular shape  $SiO_2$  buffer layer with an apex facing a notch tip, an initiated crack meanders two adjacent sides of the triangle due to refraction at the edges of the triangle as if total internal reflection (TIR) of optical wave (Fig. 5a). In terms of amplitude, this can be seen as amplification. The degree of amplification is governed by the angle of apex,  $\theta$ . As shown in Fig. 5b, the angle  $\phi$ , between the interface and the crack tip determines the final propagation direction of the crack. When  $\phi$  is sufficiently large (>15°), the crack kinks back into the buffer layer region and starts propagating to the opposite direction. This process is repeated, in turn unique and sophisticated nano-patterns are fabricated, as shown in Fig. 5a. When the crack tip angle coincide with the boundary of the  $SiO_2$  buffer layer region where  $\phi$  is <15°, the crack propagates along the edge of the buffer layer. Despite the principle propagation direction of the straight crack is [110] on (100) Si wafer, the straight crack propagates along the edge as shown in Fig. 5c. This phenomenon resembles evanescent wave of optical wave.

There are various potential applications of the nanopatterns fabricated by precise manipulation of the oscillatory cracks. Besides the complex micropatterned letters with nanoscale width demonstrated in Fig. 2a, other fast and feasible application is to use the cracks as a nanochannel. Since the crack itself is naturally a channel structure with nanoscale width, it can easily be replicated by polydimethylsiloxane (PDMS) at wafer scale as shown in Fig. 6a,b. A magnified optical image of a specific area, shown in Fig. 6b, reveals a transferred crack pattern to PDMS. A three dimensional profile obtained via AFM

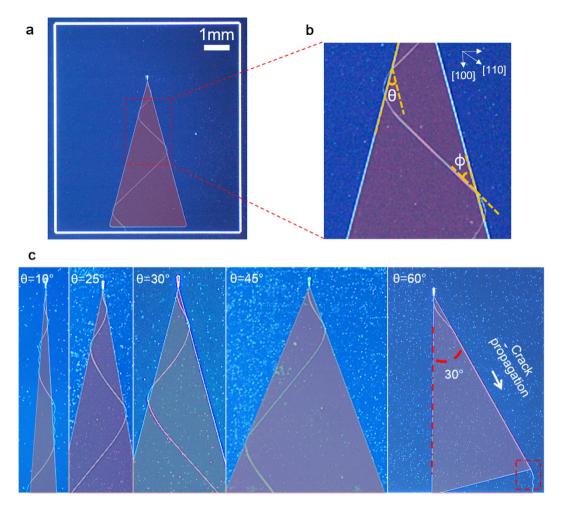


Figure 5. Optical wave-like behavior of oscillatory nanocracks and their amplitude amplification. (a,b) Optical image of amplified oscillatory crack and its magnified view. (c) Optical images of ascending amplification of the wave of a sinusoidal nanocrack for various apex angles of the buffer  $SiO_2$  pattern and the crack propagated along the interface.

(atomic force microscopy), shown in Fig. 6c, confirms that the embossed feature is the transferred crack pattern. Figure 6d shows a multi-dimension oscillating micro channel fabricated by subsequent wet etching of oscillatory cracks. Generally, adhesion between masking layer and substrate is important for microfluidic channel fabrication by wet etching, otherwise an additional deposition process is required. Usually, a metal layer on silicon substrate is patterned via lift-off process. Moreover, the minimum pattern resolution of this process is up to  $2\,\mu m$  using conventional aligner, thus creating features less than the minimum resolution is difficult especially when using isotropic wet etching. However, the  $Si_3N_4$  film adheres to Si substrate solidly as a masking layer, less than  $2\,\mu m$  features can be made with simple conventional etchant: HNA (HF/Nitric/Acetic) solution as shown in Fig. 6d²6. Strong adhesion of the  $Si_3N_4$  film to Si substrate also facilitates repetitive wet etching without additional deposition or patterning. Figure 6e shows the result of multiple isotropic wet etching. The channel width can be expanded up to several hundreds of micrometers under 10 min total etching time.

The channels fabricated by wet etching of the cracks are directly applicable to nano/micro fluidics. As shown in Fig. 6f, smooth top surface of the substrate is advantageous for PDMS or glass to adhere well for closed channel fabrication. An expanded channel capped with a PDMS block is filled with quantum dot solution as shown in Fig. 6f. Unlike isotropic wet etching, use of anisotropic etchant makes a triangular channel section profile (see supplementary information Fig. 7s). The Si-to-Si<sub>3</sub>N<sub>4</sub> etch selectivity of anisotropic etchant such as KOH or TMAH is much higher than that of HNA solution, in turn the Si<sub>3</sub>N<sub>4</sub> thin film adjacent to crack opening does not collapse or fracture after etching. This is advantageous since the channel can be capped simply by bonding or spin coating. In general, fabrication of nano/microchannels equivalent to those demonstrated in this study in width and length will be even difficult with conventional lithography or electron beam lithography which is expensive, time consuming and not scalable to large size. Using manipulation of the cracks obviates such difficulties and dramatically

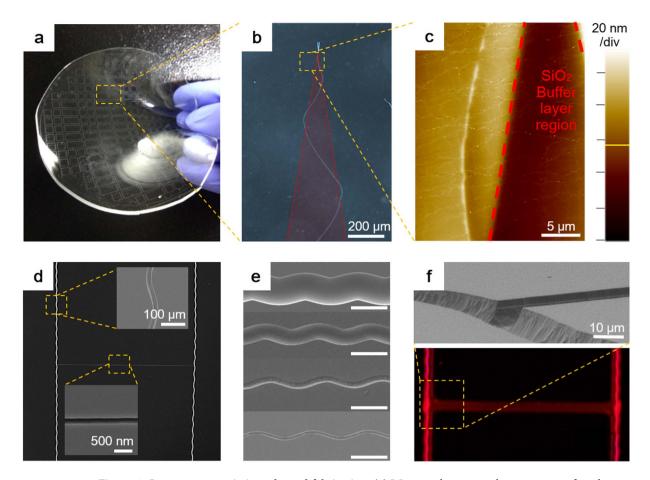


Figure 6. Large area nano/micro channel fabrication. (a) Waver scale nanocrack patterns transferred to polydimethylsiloxane (PDMS). (b) Optical microscopic image of inversed pattern of a nanocrack on PDMS. (c) Atomic force microscope (AFM) image of transferred nanocrack on PDMS. (d) Multi-scale H-shaped nano/micro channel fabricated by selective wet etching of existing cracks on (110) Si substrate. (e) SEM images of etched oscillatory cracks having various width. The inset scale bar is 100 μm. (f) SEM image of the intersection of widened channel by wet etching (top). Florescence image of quantum dot nanofluidic in the widened H-shaped channel fabricated from the controlled nanocrack (bottom).

simplify the process because cracks are self-generating in nature and can propagate all over the substrate and therefore easily scalable to very large size which is impossible with conventional processes.

#### Conclusion

We have shown fabrication of various sophisticated nanopatterns by manipulating oscillatory cracks via  $\mathrm{Si}_3\mathrm{N}_4$  film and  $\mathrm{SiO}_2$  buffer layer. The oscillatory crack shows optical wave-like characteristics such as refraction, reflection, amplification, total internal reflection, and evanescent wave. These unique optical wave like characteristics of oscillatory crack are closely related to material and mechanical properties of the substrate and the film. Strategic modification of these mechanical and material properties allows elaborate control of the crack generation. Utilizing the crack and crack control, various sophisticated nanopatterns have been demonstrated at wafer scale. In addition, the nanopatterns fabricated in this fashion offer distinct advantages over conventional nano/microchannel fabrication methods in terms of simplicity, cost and scalability. We expect that our study can be used in many fields including nanofluidics and nanoelectronics which may require very long nanochannels with high consistency in LER and LWR.

### Methods

Silicon substrates with three different crystallographic orientations ((100), (110), and (111)) were cleaned by following standard wafer cleaning procedure. Notch-like structures were patterned on the substrates via photolithography followed by reactive ion etching (RIE). Subsequently, a 200 nm-thick silicon dioxide (SiO<sub>2</sub>) thin film layer was grown on the patterned substrate by thermal oxidation. The final SiO<sub>2</sub> patterns were defined by photolithographic patterning followed by RIE. In order to control the thickness of SiO<sub>2</sub> patterns, the substrates were submerged in a diluted HF solution (1:20). Various designs of the silicon dioxide film patterns were employed in the anticipated directions of crack propagation. As a last step, a

stoichiometric silicon nitride  $(Si_3N_4)$  film was deposited on the substrates by chemical vapor deposition (CVD). The temperature and pressure condition of CVD were 800 °C and 200 mtorr, respectively. In order to induce high intrinsic stress on the silicon nitride film, dichlorosilane  $(H_2SiCl_2)$  and ammonia  $(NH_3)$  at  $30\,\mathrm{cm^3\,min^{-1}}$  (STP) and  $100\,\mathrm{cm^3\,min^{-1}}$  (STP) were used respectively. The cracks self-propagate on the substrates during the CVD process. When the substrates were taken out, the final crack patterns were defined, thus no additional process is required. The substrates were then characterized and analyzed by scanning electron microscopy (SEM) and optical microscope.

#### References

- 1. Striemer, C. C., Gaborski, T. R., McGrath, J. L. & Fauchet, P. M. Charge-and size-based separation of macromolecules using ultrathin silicon membranes. *Nature* **445**, 749–753 (2007).
- 2. Son, Y. *et al.* Nanoscale electronics: digital fabrication by direct femtosecond laser processing of metal nanoparticles. *Adv. Mater.* **23**, 3176–3181 (2011).
- 3. Tans, S. J., Verschueren, A. R. & Dekker, C. Room-temperature transistor based on a single carbon nanotube. *Nature* **393**, 49–52 (1998).
- 4. Chen, Y. & Pepin, A. Nanofabrication: Conventional and nonconventional methods. Electrophoresis 22, 187-207 (2001).
- 5. Watt, F., Bettiol, A., Van Kan, J., Teo, E. & Breese, M. Ion beam lithography and nanofabrication: a review. *Int. J. Nanosci.* 4, 269–286 (2005).
- 6. Vieu, C. et al. Electron beam lithography: resolution limits and applications. Appl. Surf. Sci. 164, 111-117 (2000).
- 7. Tseng, A. A., Notargiacomo, A. & Chen, T. Nanofabrication by scanning probe microscope lithography: A review. J. Vac. Sci. Technol. B 23, 877–894 (2005).
- 8. Binnig, G., Quate, C. F. & Gerber, C. Atomic force microscope. Phys. Rev. Lett. 56, 930 (1986).
- 9. Xia, Y. & Whitesides, G. M. Soft lithography. Annu. Rev. Mater. Sci. 28, 153-184 (1998).
- 10. Whitesides, G. M. & Grzybowski, B. Self-assembly at all scales. Science 295, 2418-2421 (2002).
- 11. Park, M., Harrison, C., Chaikin, P. M., Register, R. A. & Adamson, D. H. Block copolymer lithography: periodic arrays of ~1011 holes in 1 square centimeter. *Science* **276**, 1401–1404 (1997).
- 12. Chou, S. Y., Krauss, P. R. & Renstrom, P. J. Nanoimprint lithography. J. Vac. Sci. Tech. B 14, 4129-4133 (1996).
- 13. Yuse, A. & Sano, M. Transition between crack patterns in quenched glass plates. Nature 362, 329-331 (1993).
- 14. Zhu, X. et al. Fabrication of reconfigurable protein matrices by cracking. Nat. Mater. 4, 403-406 (2005).
- 15. Xu, B.-Y., Xu, J.-J., Xia, X.-H. & Chen, H.-Y. Large scale lithography-free nano channel array on polystyrene. Lab. Chip. 10, 2894–2901 (2010).
- Han, B. et al. Uniform self-forming metallic network as a high-performance transparent conductive electrode. Adv. Mater. 26, 873–877 (2014).
- 17. Rao, K., Gupta, R. & Kulkarni, G. U. Fabrication of Large Area, High-Performance, Transparent Conducting Electrodes Using a Spontaneously Formed Crackle Network as Template. *Adv. Mater. Interf.* 1, 140090 (2014).
- 18. Deegan, R. D. et al. Wavy and rough cracks in silicon. Phys. Rev. E 67, 066209 (2003).
- 19. Zhang, Z. et al. Wavy cleavage fracture of bulk metallic glass. Appl. Phys. Lett. 89, 251917 (2006).
- Gorokhov, E., Prinz, V. Y., Noskov, A. & Gavrilova, T. A Novel Nanolithographic Concept Using Crack-Assisted Patterning and Self-Alignment Technology. J. Electrochem. Soc. 145, 2120–2131 (1998).
- 21. Pease, L. F., Deshpande, P., Wang, Y., Russel, W. B. & Chou, S. Y. Self-formation of sub-60-nm half-pitch gratings with large areas through fracturing. *Nat. Nanotech.* **2**, 545–548 (2007).
- 22. Kim, B. C., Moraes, C., Huang, J., Thouless, M. & Takayama, S. Fracture-based micro-and nanofabrication for biological applications. *Biomater. Sci.* 2, 288–296 (2014).
- 23. Nam, K. H., Park, I. H. & Ko, S. H. Patterning by controlled cracking. Nature 485, 221-224 (2012).
- 24. Kim, D., Makaram, P. & Thompson, C. V. Microscale oscillating crack propagation in silicon nitride thin films. *Appl. Phys. Lett.* **97**, 071902 (2010).
- 25. Irene, E. Residual stress in silicon nitride films. J. Electron. Mater. 5, 287-298 (1976).
- Elliman, R., Spooner, M., Dall, T., Kim, T. & Fletcher, N. Oscillating cracks in glassy films on silicon substrates. *Philos. Mag.* 87, 4893–4906 (2007).
- 27. Ye, T., Suo, Z. & Evans, A. Thin film cracking and the roles of substrate and interface. Int. J. Solids Struct. 29, 2639–2648 (1992).
- 28. Danilewsky, A. et al. Crack propagation and fracture in silicon wafers under thermal stress. J. Appl. Crystallogr. 46, 849–855 (2013).
- 29. Beuth, Jr, J. Cracking of thin bonded films in residual tension. Int. J. Solids Struct. 29, 1657-1675 (1992).

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## **Author Contributions**

Y.D.S., J.Y. and S.H.K. designed the experiments. Y.D.S. and J.Y. prepared the sample. Y.D.S., H.L., S.H., J.K. and K.K. conducted various characterization of the sample. Y.D.S., J.Y. and S.H.K. co-wrote the manuscript.

# **Additional Information**

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