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## Operating region-dependent characteristics of weight updates in synaptic In–Ga–Zn–O thin-film transistors

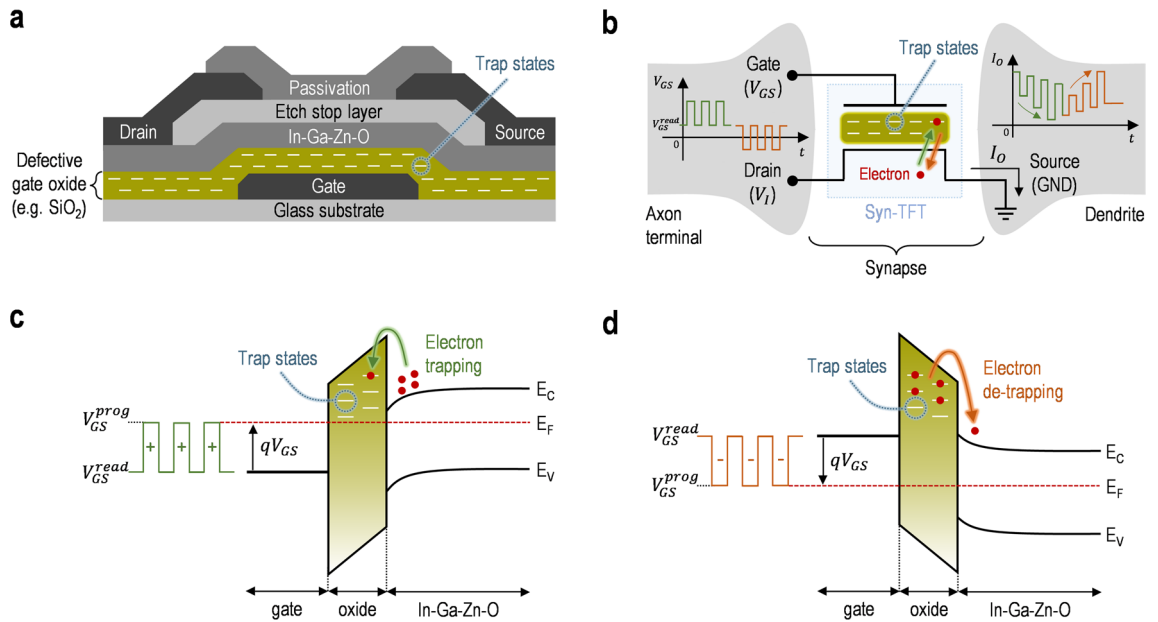
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We present a study on characteristics of operating region-dependent weight updates in a synaptic thin-film transistor (Syn-TFT) with an amorphous In–Ga–Zn–O (IGZO) channel layer. For a synaptic behavior (e.g. a memory phenomenon) of the IGZO TFT, a defective oxide (e.g. SiO<sub>2</sub>) is intentionally used for a charge trapping due to programming pulses to the gate terminal. Based on this synaptic behavior, a conductance of the Syn-TFT is modulated depending on the programming pulses, thus weight updates. This weight update characteristics of the Syn-TFT is analyzed in terms of a dynamic ratio ( $dr_w$ ) for two operating regions (i.e. the above-threshold and sub-threshold regimes). Here, the operating region is chosen depending on the level of the gate read-voltage relative to the threshold voltage of the Syn-TFT. To verify these, the static and pulsed characteristics of the fabricated Syn-TFT are monitored experimentally. As experimental results, it is found that the  $dr_w$  of the sub-threshold regime is larger compared to the above-threshold regime. In addition, the weight linearity in the sub-threshold regime is observed to be better compared to the above-threshold regime. Since it is expected that either the  $dr_w$  or weight linearity can affect performances (e.g. a classification accuracy) of an analog accelerator (AA) constructed with the Syn-TFTs, the AA simulation is performed to check this with a crossbar simulator.

An emerging technology based on non-silicon material has been intensively studied for the futuristic applications, such as a brain-inspired neuromorphic system<sup>1–5</sup>. Here, as a fundamental building block of the neuromorphic system, synaptic devices with the emerging material have been reported<sup>6–10</sup>. For example, thin-film transistors (TFTs) based on an amorphous oxide semiconductors (AOSs), such as In–Ga–Zn–O (IGZO), with a high mobility have been considered as a synaptic device because of their optical and electrical instabilities, mimicking the properties of a biological synapse (e.g. a memory phenomenon).<sup>11–15</sup> Note that, the optical and electrical instabilities have been believed to be poor properties of the IGZO TFTs for displays<sup>16,17</sup>. However, for the synaptic properties of the IGZO TFTs, electrical instabilities (e.g. defects in the gate oxide) among these are intentionally used. IGZO TFTs with these synaptic properties are conventionally operated at the above-threshold regime due to a relatively high current driving capability<sup>18–21</sup>. This may be expected that the speed of the weight update in the above-threshold regime is faster compared to the sub-threshold regime. However, the above-threshold regime can have a narrow synaptic dynamic-range due to a linear (or parabolic) dependence of the drain current at its regime<sup>22,23</sup>. In addition, for the case of the above-threshold regime, it is expected that the power dissipation is difficult to be decreased because of its relatively high current level<sup>24–26</sup>. On the other hand, the sub-threshold regime of the IGZO TFT can achieve an ultra-low power consumption<sup>27,28</sup>. However, it can be a relatively slower speed of the weight updates due to a lower current level compared to the above-threshold regime, although the speed of the weight updates at the sub-threshold regime can also be reasonable for a neuronal time scale of the human brain which is very long with a range from milliseconds to hundreds of milliseconds<sup>29,30</sup>. Besides these power consumption and speed, it can also be expected that two operating regions are to be different each other in terms of the characteristics of weight updates, such as a dynamic ratio and weight linearity, so it needs to be studied.

In this paper, a study on the characteristics of operating region-dependent weight updates with a synaptic thin-film transistor (Syn-TFT) based on an amorphous IGZO channel layer is presented. In order to achieve a memory function of the IGZO TFT, the defective oxides (e.g. SiO<sub>2</sub>) can be intentionally used for a charge trapping because of programming pulses to the gate terminal ( $V_{GS}$ ). Based on this memory behavior, a conductance of the

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**Figure 1.** (a) Schematic cross-sectional view and (b) equivalent circuit of the Syn-TFT. Note that the axon terminal can be considered as the  $V_{GS}$ . Band diagrams to describe (c) the electron trapping with applying positive programming pulses and (d) electron de-trapping with applying negative programming pulses. Here, the  $E_c$  is the conduction band minima,  $E_v$  is the valence band maxima, and  $E_f$  is the Fermi level.

Syn-TFT is varied, which is dependent on the programming pulses, thus a weight updates. This characteristics of weight updates is analyzed for two operating regions, such as the above- threshold and sub-threshold regimes, in terms of a dynamic ratio ( $dr_w$ ). Here, the operational regime can be determined depending on the level of the gate read-voltage ( $V_{GS}^{read}$ ) relative to the threshold voltage ( $V_T$ ) of the Syn-TFT. To check these, a static and pulsed characteristics of the fabricated Syn-TFT are experimentally monitored using a standard semiconductor analyzer. For the experimental results, it is shown that the  $dr_w$  of the sub-threshold regime is larger compared to the above-threshold regime. Moreover, the linear range of the weight in the sub-threshold regime is wider compared to the above-threshold regime. Either the  $dr_w$  or weight linearity is expected to affect performances (e.g. a classification accuracy (CA)) of an analog accelerator (AA) where Syn-TFTs are arrayed. To verify this, the AA simulation is performed with a crossbar simulator.

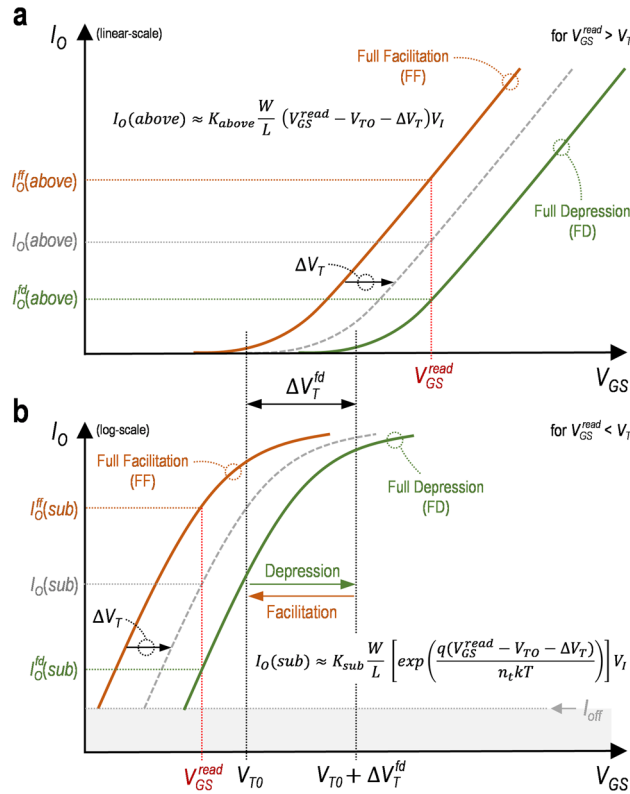
### Syn-TFT and related theories

**Synaptic behavior of the IGZO thin-film transistor.** Figure 1a,b show a cross-sectional view of the Syn-TFT (i.e. the IGZO TFT) and its equivalent circuit, respectively. As can be seen, the defects in the gate oxide (e.g.  $SiO_2$ ) of the Syn-TFT can be given due to the disorder of the oxide deposited with a low-temperature process for the TFT<sup>31</sup>. In this defective oxide, multiple trap states can exist, where charges (e.g. electrons) in the IGZO channel layer can be trapped or de-trapped with applying programming pulses to the gate terminal ( $V_{GS}$ ), so the memory function is achieved. This memory function is depicted in Fig. 1c,d in detail with the band diagrams for the electron trapping and de-trapping, respectively. Here, the electron trapping or de-trapping is dependent on the polarity of the programming voltage ( $V_{GS}^{prog}$ ) and the number of programming pulses applied to the gate for a fixed input voltage ( $V_I$ ) at the drain. Due to these trapped or de-trapped electrons, the threshold voltage shift ( $\Delta V_T$ ) can be varied, which is defined as  $\Delta Q_e^{trap}/C_i$ . Here, the  $\Delta Q_e^{trap}$  is the variation of the negative charge density per area and  $C_i$  is a gate insulator capacitance per area. So, the  $V_T$  can also be varied and expressed as follows,

$$V_T = V_{T0} + \Delta V_T, \tag{1}$$

where the  $V_{T0}$  is an initial value of the  $V_T$ . For the initial state, assuming that there are no trapped electrons in the gate insulator,  $\Delta V_T = 0$ , thus  $V_T = V_{T0}$ . And, for the case with applying multiple programming pulses (e.g. positive or negative programming pulses), the  $\Delta V_T$  can be varied due to the electron trapping or de-trapping, thus  $V_T = V_{T0} + \Delta V_T$ . With this  $V_T$ , the  $V_{GS}^{read}$  can be set as  $V_{GS}^{read} > V_T$  or  $V_{GS}^{read} < V_T$  to select the operating region (i.e. the above-threshold region or sub-threshold region). Figure 2 describes the schematic transfer characteristics in the above-threshold and sub-threshold regions, respectively. Here, assuming a sufficiently low  $V_I$  in two operating regions, the output current ( $I_O$ ) of respective operational regimes is approximated as follows<sup>32</sup>,

$$I_{O(above)} \approx K_{above} \frac{W}{L} \left( V_{GS}^{read} - V_{T0} - \Delta V_T \right) V_I \quad \text{for } V_{GS}^{read} > V_T, \tag{2}$$



**Figure 2.** Conceptual plots of the transfer characteristics in (a) the above-threshold regime and (b) sub-threshold regime for synaptic processes (e.g. the synaptic depression and facilitation) with threshold voltage shifts varied upon programming pulses. Here, the full facilitation and full depression as two extreme states are described for respective operational regime. And the  $I_{O(above)}$  and  $I_{O(sub)}$  are shifted toward the full depression with the increase of the  $\Delta V_T$ , respectively (gray dashed lines). In addition, for the case of the sub-threshold regime, the transfer characteristics is depicted as the semi-log plot to clearly show its trend. Note that the  $I_{off}$  is the current off floor.

$$I_{O(sub)} \approx K_{sub} \frac{W}{L} \left[ \exp \left( \frac{q(V_{GS}^{read} - V_{T0} - \Delta V_T)}{n_t kT} \right) \right] V_I \quad \text{for } V_{GS}^{read} < V_T, \tag{3}$$

where the  $K_{above}$  is a constant for the above-threshold regime,  $K_{sub}$  is a constant for the sub-threshold regime,  $L$  is the channel length,  $W$  is the channel width,  $n_t$  is the ideality factor related to interface states,  $kT$  is the thermal energy, and  $q$  is the elementary charge. As seen in Fig. 2, the initial state of the Syn-TFT is a full facilitation (FF) for a maximum value of the  $I_O$  (i.e.  $I_O^{ff}$ ) with  $V_T = V_{T0}$  (i.e.  $\Delta V_T = 0$ ). So, the  $I_O^{ff}$  can be rewritten with Eqs. (2) and (3), as follows,

$$I_{O(above)}^{ff} \approx K_{above} \frac{W}{L} (V_{GS}^{read} - V_{T0}) V_I \quad \text{for } V_{GS}^{read} > V_T, \tag{4}$$

$$I_{O(sub)}^{ff} \approx K_{sub} \frac{W}{L} \left[ \exp \left( \frac{q(V_{GS}^{read} - V_{T0})}{n_t kT} \right) \right] V_I \quad \text{for } V_{GS}^{read} < V_T, \tag{5}$$

where the  $I_{O(above)}^{ff}$  is the  $I_O^{ff}$  for the above-threshold regime and  $I_{O(sub)}^{ff}$  is the  $I_O^{ff}$  for the sub-threshold regime. From the FF, with applying multiple positive programming pulses to the gate, the  $I_O$  can be gradually decreased due to the increase of the  $V_T$ , which is called a synaptic depression process. Here, for the synaptic depression, the increase of the  $\Delta V_T$  is limited with applying more programming pulses, thus the saturated  $\Delta V_T$ . At that moment, the state of the decreased  $I_O$  becomes a full depression (FD), defining the maximized  $\Delta V_T = \Delta V_T^{fd} > 0$  (see Fig. 2). Here, the  $\Delta V_T^{fd}$  is the saturated  $\Delta V_T$  with respect to the time although the  $\Delta V_T$  can be further enhanced with more programming pulses. Similarly, the  $I_O$  for this FD (i.e.  $I_{O(above)}^{fd}$ ) can be represented with Eqs. (2) and (3), as follows,

$$I_{O(above)}^{fd} \approx K_{above} \frac{W}{L} (V_{GS}^{read} - V_{T0} - \Delta V_T^{fd}) V_I \quad \text{for } V_{GS}^{read} > V_T, \tag{6}$$

$$I_O^{\text{fd}}(\text{sub}) \approx K_{\text{sub}} \frac{W}{L} \left[ \exp \left( \frac{q(V_{\text{GS}}^{\text{read}} - V_{\text{T0}} - \Delta V_{\text{T}}^{\text{fd}})}{n_t kT} \right) \right] V_1 \quad \text{for } V_{\text{GS}}^{\text{read}} < V_{\text{T}}. \quad (7)$$

Here, the  $I_O^{\text{fd}}(\text{above})$  is the  $I_O^{\text{fd}}$  for the above-threshold regime and  $I_O^{\text{fd}}(\text{sub})$  is the  $I_O^{\text{fd}}$  for the sub-threshold regime. Note that, with applying multiple negative programming pulses from the FD condition, the  $I_O$  can be recovered to the initial state (i.e. the FF) because of the decreased  $V_T$ , which is called a synaptic facilitation process.

**Synaptic weight and dynamic ratio.** Based on the synaptic behavior explained in the previous section, the characteristics of weight updates can be theoretically explained for two operating regions. As illustrated in the equivalent circuit of the Syn-TFT (see Fig. 1b), the conductance as a synaptic weight ( $G_w$ ) can be defined as a ratio between the  $V_I$  and  $I_O$ , as follows,

$$G_w \equiv \frac{I_O}{V_I}. \quad (8)$$

Based on this Eq. (8), the  $G_w$  for the FF and FD at  $V_{\text{GS}} = V_{\text{GS}}^{\text{read}}$  can be expressed with  $I_O^{\text{ff}}$  and  $I_O^{\text{fd}}$ , respectively, as follows,

$$G_w^{\text{ff}} = \frac{I_O^{\text{ff}}}{V_I}, \quad (9)$$

$$G_w^{\text{fd}} = \frac{I_O^{\text{fd}}}{V_I}, \quad (10)$$

where  $G_w^{\text{ff}}$  is the  $G_w$  for the FF and  $G_w^{\text{fd}}$  is the  $G_w$  for the FD. Here, for a detailed comparison between two operating regions, a normalized synaptic weight ( $\overline{w_G}$ ) needs to be defined. So, using the  $G_w^{\text{ff}}$  as the maximum value of the  $G_w$ , the  $\overline{w_G}$  can be represented with Eqs. (8) and (9), as follows,

$$\overline{w_G} \equiv \frac{G_w}{G_w^{\text{ff}}} = \frac{I_O}{I_O^{\text{ff}}}. \quad (11)$$

With Eqs. (2) and (4), the  $\overline{w_G}$  in the above-threshold region (i.e.  $\overline{w_G}(\text{above})$ ) can be represented based on Eq. (11), as follows,

$$\overline{w_G}(\text{above}) \approx \frac{V_{\text{GS}}^{\text{read}} - V_{\text{T0}} - \Delta V_{\text{T}}}{V_{\text{GS}}^{\text{read}} - V_{\text{T0}}} \quad \text{for } V_{\text{GS}}^{\text{read}} > V_{\text{T}}. \quad (12)$$

Similarly, the  $\overline{w_G}$  in the sub-threshold region (i.e.  $\overline{w_G}(\text{sub})$ ) can be represented with Eqs. (3) and (5), as follows,

$$\overline{w_G}(\text{sub}) \approx \exp \left( \frac{-q\Delta V_{\text{T}}}{n_t kT} \right) \quad \text{for } V_{\text{GS}}^{\text{read}} < V_{\text{T}}. \quad (13)$$

Here, the  $\overline{w_G}(\text{above})$  is given as a linear function of the  $\Delta V_{\text{T}}$  whereas the  $\overline{w_G}(\text{sub})$  has an exponential dependence with the  $\Delta V_{\text{T}}$ . As the two extreme cases of  $\overline{w_G}$  commonly for both operating regions, the  $\overline{w_G}$  for the FF (i.e.  $I_O = I_O^{\text{ff}}$ ) and FD (i.e.  $I_O = I_O^{\text{fd}}$ ) is expressed based on Eq. (11), respectively, as follows,

$$\overline{w_G}^{\text{ff}} = 1, \quad (14)$$

$$\overline{w_G}^{\text{fd}} = \frac{I_O^{\text{fd}}}{I_O^{\text{ff}}}, \quad (15)$$

where the  $\overline{w_G}^{\text{ff}}$  is the  $\overline{w_G}$  for the FF and  $\overline{w_G}^{\text{fd}}$  is the  $\overline{w_G}$  for the FD. As can be seen in Eq. (15), it is expected that the  $\overline{w_G}^{\text{fd}}$  approaches zero since  $I_O^{\text{ff}} \gg I_O^{\text{fd}}$ .

As another synaptic characteristics, the  $dr_w$  can also be defined as a ratio between the  $\overline{w_G}^{\text{ff}}$  and  $\overline{w_G}^{\text{fd}}$  with Eqs. (14) and (15), as follows,

$$dr_w \equiv \frac{\overline{w_G}^{\text{ff}}}{\overline{w_G}^{\text{fd}}} = \frac{I_O^{\text{ff}}}{I_O^{\text{fd}}}. \quad (16)$$

Note that, for the ideal case, the  $dr_w$  is infinity because the  $\overline{w_G}^{\text{ff}}$  is unity and  $\overline{w_G}^{\text{fd}}$  is zero, so a large  $dr_w$  is advantageous. Likewise, with Eqs. (4) and (6), the  $dr_w$  in the above-threshold region (i.e.  $dr_w(\text{above})$ ) is expressed based on Eq. (16), as follows,

$$dr_w(\text{above}) \approx \frac{V_{GS}^{\text{read}} - V_{T0}}{V_{GS}^{\text{read}} - V_{T0} - \Delta V_T^{\text{fd}}} \quad \text{for } V_{GS}^{\text{read}} > V_T. \quad (17)$$

Similarly, the  $dr_w$  in the sub-threshold region (i.e.  $dr_w(\text{sub})$ ) is represented with Eqs. (5) and (7), as follows,

$$dr_w(\text{sub}) \approx \exp\left(\frac{q\Delta V_T^{\text{fd}}}{n_t kT}\right) \quad \text{for } V_{GS}^{\text{read}} < V_T. \quad (18)$$

Here, the  $dr_w(\text{sub})$  is expected to be larger compared to the  $dr_w(\text{above})$  because of the exponential dependence in the sub-threshold regime despite of the same  $\Delta V_T = \Delta V_T^{\text{fd}}$  in two operating regions, implying an advantage of the sub-threshold regime.

Note that, as another advantage of the sub-threshold regime, since the current level in this region is lower compared to the above-threshold regime, it is expected that the static power consumption at  $V_{GS} = V_{GS}^{\text{read}}$  ( $P_{\text{static}}^{\text{read}}$ ) is relatively low. The  $P_{\text{static}}^{\text{read}}$  can be represented for a fixed  $V_b$  as follows<sup>33</sup>,

$$P_{\text{static}}^{\text{read}} = I_O V_I. \quad (19)$$

And the  $P_{\text{static}}^{\text{read}}(\text{max})$  as a maximum value of the  $P_{\text{static}}^{\text{read}}$  can be expressed as a product of  $I_O^{\text{ff}}$  and  $V_b$ , as follows,

$$P_{\text{static}}^{\text{read}}(\text{max}) = I_O^{\text{ff}} V_I. \quad (20)$$

Based on the theory with respect to the Syn-TFT, since the static and pulsed characteristics are expected to be different for two operating regions, these characteristics of the fabricated Syn-TFT need to be monitored experimentally.

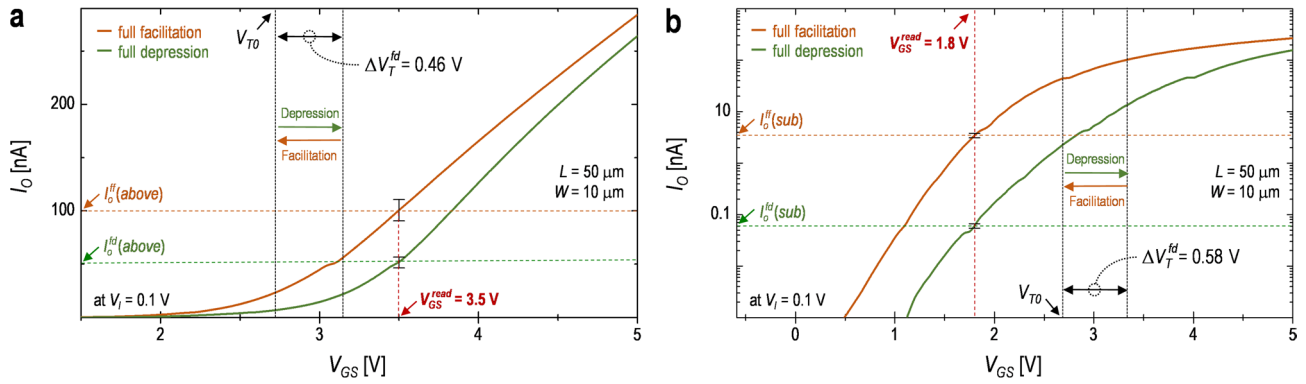
## Results and discussion

Based on the theoretical analysis in the previous section, it is expected that the synaptic behavior of the IGZO TFT in two operating regions can be achieved due to the trap states in its defective oxide (e.g.  $\text{SiO}_2$ ), thus the Syn-TFT. The fabrication process of the Syn-TFT is given in following “Materials and fabrication process of the Syn-TFT”<sup>27</sup>. In addition, to verify memory, experimental results of the static and pulsed characteristics of the fabricated Syn-TFT with the IGZO channel layer are described in the following “Selection of the gate read-voltage to determine the operating region” and “Pulsed characteristics of the Syn-TFT”, respectively. For the static characteristics of “Selection of the gate read-voltage to determine the operating region”, the operating region is determined depending on the level of the  $V_{GS}^{\text{read}}$  relative to the  $V_T$ . In “Pulsed characteristics of the Syn-TFT”, the synaptic behavior, such as the weight updates, is analyzed in terms of the  $dr_w$  for two operating regions. Here, the experimental framework is performed with a standard semiconductor analyzer, called Keithley™ 4200A. As another observation of the weight updates characteristics, the weight linearity can be analyzed for two operational regimes. In order to check this, the plots of  $\Delta \overline{w_G}$  versus  $\overline{w_G}$  are shown in the following “Weight linearity of the Syn-TFT”.

In addition, when an array composed of Syn-TFTs is used as an analog accelerator (AA), the performances (e.g. a classification accuracy (CA)) of the AA are expected to be different depending on the operating region. For this, the comparison of the CA as a training result for a classification task is presented in “Analog accelerator based on the Syn-TFT array”. Here, the CA is checked with the AA simulation based on the weight updates monitored with the fabricated Syn-TFT, using a resistive memory simulator, called CrossSim™<sup>34–37</sup>.

**Materials and fabrication process of the Syn-TFT.** The fabrication process of the Syn-TFT is as follows. On the glass wafer, RF-sputtering is performed for Molybdenum (Mo) deposition for the gate electrode, assuming that the work-function of Mo is 5 eV<sup>27</sup>. This is followed by wet etching to pattern the gate electrode. And then,  $\text{SiO}_2$  are layered with plasma enhanced chemical vapor deposition (PECVD). The total film thickness of 350 nm is used as the gate insulator where the defects (e.g. trap states) can be given due to a low temperature process. The channel layer of the Syn-TFT is a 50 nm-thick IGZO deposited with RF-sputtering, using an IGZO ceramic target, subsequently patterned by RIE. In the sputter deposition, the oxygen-gas partial pressure against Ar (i.e.  $\text{O}_2/(\text{O}_2 + \text{Ar})$ ) is 15% to realize Syn-TFT structure, in which each was annealed at 250 °C. Then an etch-stop layer is formed using  $\text{SiO}_x$  to protect the IGZO channel layer. This is followed by dry etching to define source and drain electrode, respectively. For the final metallization, 150 nm-thick Mo is deposited with a RF sputtering, and patterned with a wet etching. Finally, the device is passivated with a 200 nm-thick  $\text{SiN}_x/\text{SiO}_x$ , which followed by another thermal annealing at 250 °C for all the samples.

**Selection of the gate read-voltage to determine the operating region.** In order to check the characteristics of the weight updates depending on the operating region (i.e. the above-threshold and sub-threshold regions), it is needed to select a proper  $V_{GS}^{\text{read}}$ . For this, the static characteristics of the fabricated Syn-TFT need to be firstly monitored for the conditions of both  $V_{GS}^{\text{read}} > V_T$  and  $V_{GS}^{\text{read}} < V_T$ . Figure 3a,b show the transfer characteristics at the two extreme states (i.e. the FF and FD) for each operational region of the fabricated Syn-TFT, respectively. As can be seen, the  $V_{GS}^{\text{read}}$  in the above-threshold and sub-threshold regimes are set as 3.5 V and 1.8 V, respectively. Note that, for the case of  $V_{GS}^{\text{read}}$  in the above-threshold regime, since the  $V_{GS}^{\text{read}}$  can get into the sub-threshold regime for the shift of the  $V_T$  with applying multiple positive programming pulses, a proper  $V_{GS}^{\text{read}}$  needs to be chosen satisfying with the condition, i.e.  $V_{GS}^{\text{read}} > V_{T0} + \Delta V_T^{\text{fd}}$ . For the FF state (i.e.  $V_T = V_{T0}$ ) with the  $V_{GS}^{\text{read}}$  determined in two operating regions, the  $I_O^{\text{ff}}(\text{above})$  and  $I_O^{\text{ff}}(\text{sub})$  at  $V_{GS} = V_{GS}^{\text{read}}$  are approximately 100 nA



**Figure 3.** Measured drain current ( $I_O$ ) as a function of the gate voltage ( $V_{GS}$ ) for the Syn-TFT at the extreme cases [e.g. the full facilitation (FF) and full depression (FD)] for  $V_I = 0.1$  V, where the gate read voltage ( $V_{GS}^{read}$ ) is set as (a)  $V_{GS}^{read} > V_{T0}$  in the above-threshold regime and (b)  $V_{GS}^{read} < V_{T0}$  in the sub-threshold regime, respectively. In addition, error bars of 10% are indicated for the  $I_O$  at the FF and FD, respectively.

and 3.5 nA, respectively. From this state, after positive programming pulses of 100 cycles are applied to the gate terminal, the  $I_O$  in two operational regions at  $V_{GS} = V_{GS}^{read}$  arrive at the FD state with  $\Delta V_T = \Delta V_T^{fd}$  due to multiple electron trapping (see Fig. 1c), resulting in  $I_O^{fd}(above) = 51$  nA and  $I_O^{fd}(sub) = 0.06$  nA, respectively. Here, it is found that the ratio between the  $I_O^{ff}(sub)$  and  $I_O^{fd}(sub)$  is larger compared to the ratio between  $I_O^{ff}(above)$  and  $I_O^{fd}(above)$ , as shown in Fig. 3a,b. This is because the sub-threshold current is dependent on the exponential function, which can be explained with Eqs. (4) to (7). On the other hand, for the case of the above-threshold regime, the current equation is a linear function (see Eq. 2), thus a relatively small ratio between the  $I_O^{ff}$  and  $I_O^{fd}$ . Note that it is found that the  $\Delta V_T^{fd}$  of two operating regions, such as the  $\Delta V_T^{fd}$  of the above-threshold regime (i.e.  $\Delta V_T^{fd}(above) = 0.46$  V) and  $\Delta V_T^{fd}$  of the sub-threshold regime (i.e.  $\Delta V_T^{fd}(sub) = 0.58$  V), are to be different each other. This can be explained with a sensitivity ( $s$ ). And the  $s$  can be represented, as follows<sup>38</sup>,

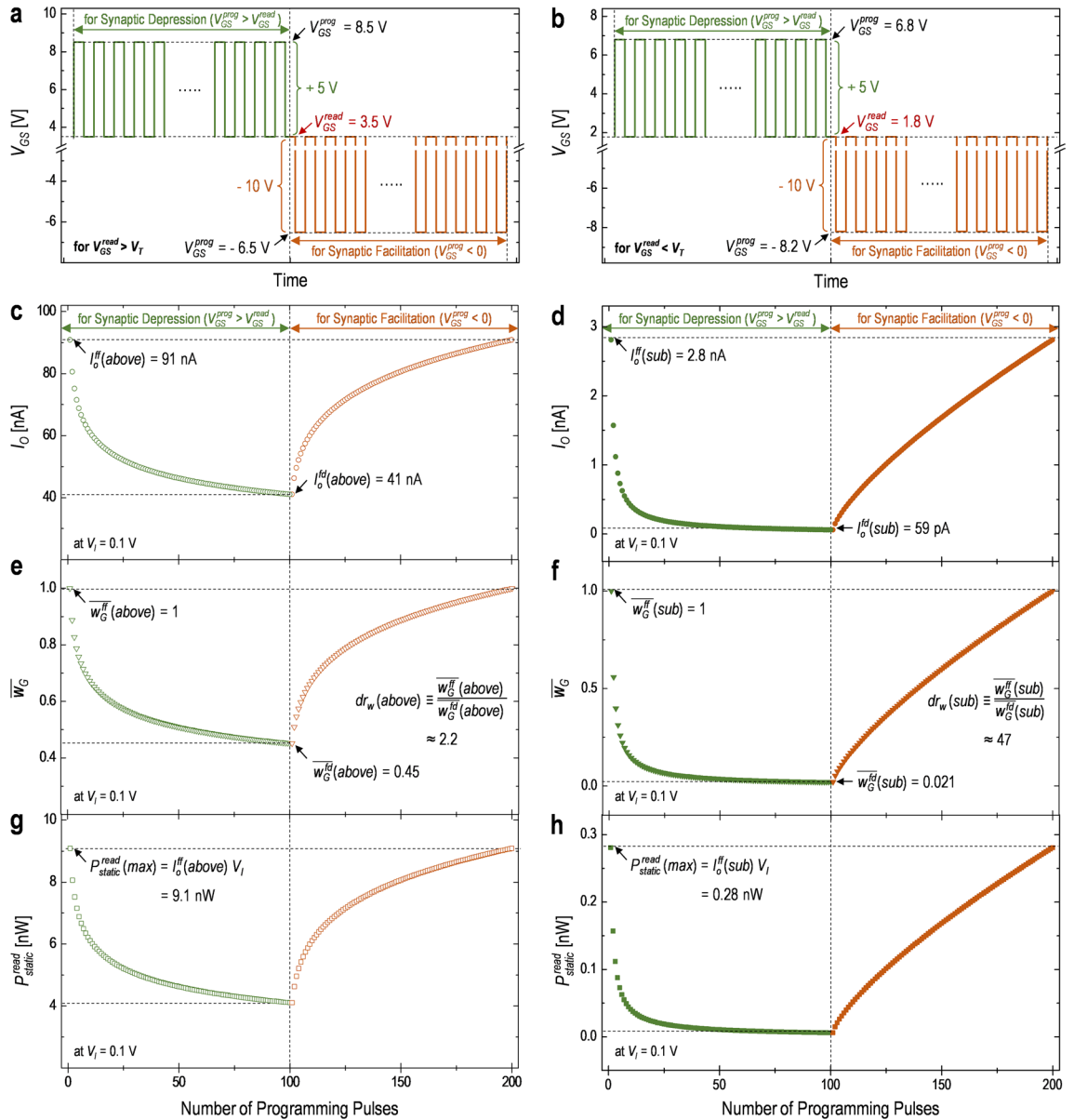
$$s \equiv \frac{\Delta n_e}{n_e^{read}}, \tag{21}$$

where the  $\Delta n_e$  is the negative charge density trapped or de-trapped from the defective gate oxide and  $n_e^{read}$  is the negative charge density in the In-Ga-Zn-O film at  $V_{GS} = V_{GS}^{read}$ . Here, since the  $n_e^{read}$  of the sub-threshold regime is smaller compared to the  $n_e^{read}$  of the above-threshold regime and the  $\Delta n_e$  of the sub-threshold regime can be bigger than the  $\Delta n_e$  of the above-threshold regime, the  $s$  of the sub-threshold regime can be larger compared to the  $s$  of the above-threshold regime, thus  $\Delta V_T^{fd}(sub) > \Delta V_T^{fd}(above)$ .

Based on the selected  $V_{GS}^{read}$  in respective operating regions, the detailed synaptic processes (i.e. the synaptic depression and facilitation) need to be monitored to verify the characteristics of the weight updates. Therefore, both the detailed pulse-specification and respective pulsed characteristics of the fabricated Syn-TFT are explained in the following section.

**Pulsed characteristics of the Syn-TFT.** Based on the analysis with respect to the static characteristics of the fabricated Syn-TFT to select the appropriate  $V_{GS}^{read}$  for each operational region, the pulsed characteristics of the device need to be monitored. Figure 4a,b show the pulse-specification for the synaptic depression and facilitation in respective operating regions. For the above-threshold regime, the  $V_{GS}^{prog}$  for the synaptic depression and facilitation is 8.5 V and  $-6.5$  V, respectively, maintaining  $V_{GS}^{read} = 3.5$  V, as seen in Fig. 4a. For the case of the sub-threshold regime (see Fig. 4b), the  $V_{GS}^{prog}$  is set as 6.8 V and  $-8.2$  V for the synaptic depression and facilitation at  $V_{GS}^{read} = 1.8$  V, respectively. Note that the pulse height (i.e. the difference between the  $V_{GS}^{prog}$  and  $V_{GS}^{read}$ ) depending on the synaptic process is commonly given for both operating regions, such as 5 V for the synaptic depression and  $-10$  V for the synaptic facilitation. In addition, the pulse width in two operating regions is commonly set as 3.08 s in the synaptic depression and 37.76 s in the synaptic facilitation, respectively. Other specifications of the programming pulses are common for two operating regions, as follows, the number of cycles and duty cycle for each synaptic process are 100 cycles and 50%, respectively.

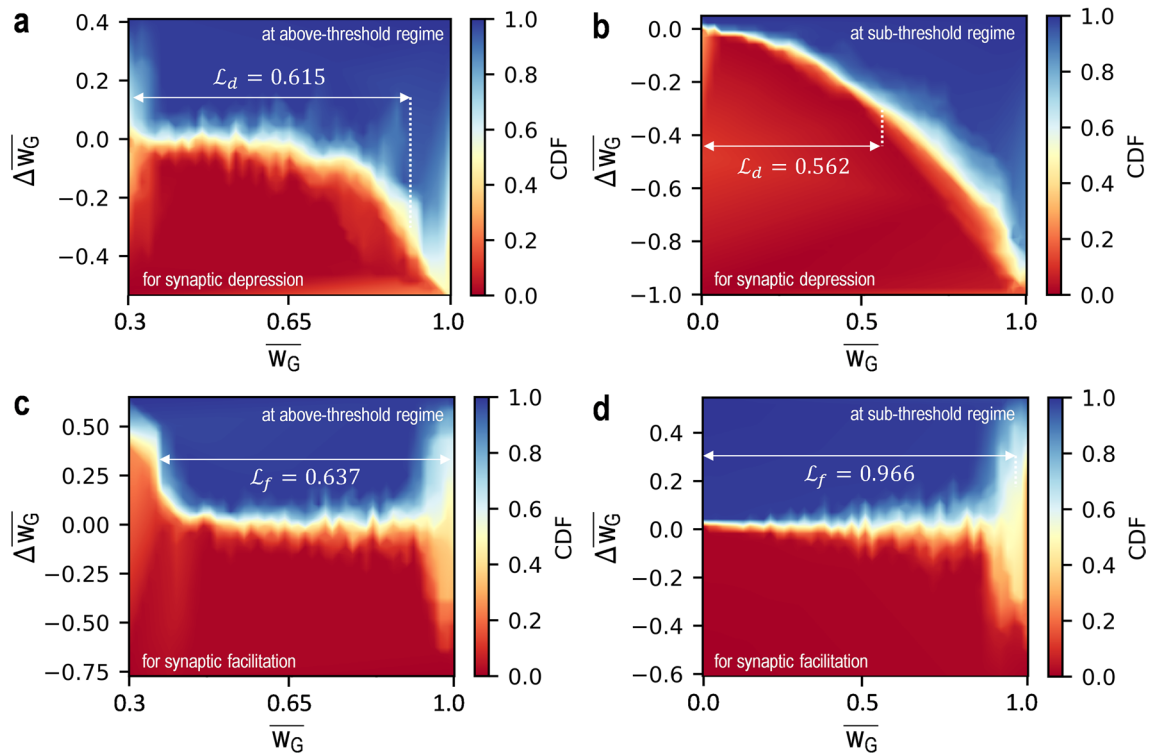
With the waveform condition of these programming pulses as seen in Fig. 4a,b, the dynamics of the  $I_O$  needs to be firstly monitored for the synaptic depression and facilitation. Figure 4c,d describe the  $I_O$  varied for the synaptic processes in two operational regimes. As can be seen, it is found that the  $I_O$  is gradually decreased with a series of positive programming pulses for the synaptic depression. It is because the  $\Delta V_T$  is increased by electrons trapped into the gate oxide (see Fig. 1c), thus the increase of the  $V_T$ . This can be explained with Eqs. (2) and (3). After 100 cycles of positive programming pulses for the synaptic depression, the  $I_O$  arrives at the FD with  $\Delta V_T^{fd}(above) = 0.46$  V and  $\Delta V_T^{fd}(sub) = 0.58$  V, respectively. As a result, the  $I_O^{fd}(above)$  and  $I_O^{fd}(sub)$  are approximately 41 nA and 0.06 nA in terms of the  $I_O$ , respectively, as indicated in Fig. 4c,d. During the synaptic facilitation, it is shown that the  $I_O$  is gradually increased with applying negative programming pulses because the  $\Delta V_T$  is reduced by electrons de-trapped out of the gate oxide (see Fig. 1d), resulting in the recovery of the  $V_T$ . This can



**Figure 4.** Waveform of the positive/negative programming pulses for the synaptic processes (e.g. a synaptic depression and facilitation) of (a) the above-threshold regime and (b) sub-threshold regime, respectively. The output current of (c) the above-threshold regime and (d) sub-threshold regime for the synaptic processes of the Syn-TFT based on the programming pulses, respectively. Here, programming pulses of 100 cycles for respective synaptic process are applied to the gate terminal. The normalized synaptic weight ( $\overline{w}_G$ ) versus the number of the programming pulses for (e) the above-threshold regime and (f) sub-threshold regime, respectively. Here, the dynamic ratio ( $dr_w$ ) of each operating region can be calculated with the  $\overline{w}_G$  of two extreme states. The calculated static-power consumption ( $P_{static}^{read}$ ) at the  $V_{GS}^{read}$  for the synaptic processes of (g) the above-threshold regime and (h) sub-threshold regime, respectively, indicating the maximum static power consumption ( $P_{static}^{read}(max)$ ).

also be explained with Eqs. (2) and (3). And the  $I_0$  is almost back to the  $I_0^{ff}(above)$  of 91 nA and  $I_0^{ff}(sub)$  of 2.8 nA, respectively, which corresponds to the FF (see Fig. 4c,d).

Based on the  $I_0$  as mentioned earlier, the characteristics of weight updates can be checked and monitored. Figure 4e,f show the behavior of the  $\overline{w}_G$  for the synaptic depression and facilitation in two operating regions. During the synaptic depression, it is shown that the trend of the  $\overline{w}_G$  decays with applying positive programming pulses due to the increased  $\Delta V_T$ , which results in the decrease of the  $I_0$ . Its decay can also be explained with Eqs. (12) and (13). At the FD, the  $\overline{w}_G$  in two operational regimes finally approaches at  $w_G^{fd}(above) = 0.45$  and  $w_G^{fd}(sub) = 0.021$ , respectively. This is because the  $I_0$  arrives at the FD state, which is consistent with Eq. (15). After positive programming pulses continued for 100 cycles, it is found that the  $\overline{w}_G$  is facilitated with a series of negative programming pulses, approaching the  $w_G^{ff}(above) \cong 1$  and  $w_G^{ff}(sub) \cong 1$ , respectively. It is because of the gradual decrease of the  $\Delta V_T$ , resulting in the increase of the  $I_0$ , which can also be explained with Eqs. (12) and (13). In addition, it is found to be a linear increase of the  $\overline{w}_G$  for the synaptic facilitation. This can specifically be



**Figure 5.** Plots of  $\Delta\overline{w}_G$  versus  $\overline{w}_G$  of (a) the above-threshold regime and (b) sub-threshold regime for the synaptic depression, respectively. In addition, for the synaptic facilitation, plots of  $\Delta\overline{w}_G$  versus  $\overline{w}_G$  of (c) the above-threshold regime and (d) sub-threshold regime, respectively. For these plots, weight updates of respective operational regime of the Syn-TFT are used and those show the cumulative distribution function (CDF) of the probabilistic variation for the  $\Delta\overline{w}_G$ . Here, the  $\mathcal{L}_d$  and  $\mathcal{L}_f$  are the linear range of the weight for the synaptic depression and facilitation, respectively. Note that 100 cycles of the programming pulses are applied to the Syn-TFT for the synaptic depression and facilitation of two operating regions, respectively.

explained with both the  $I_O$  relative to the  $\Delta V_T$  and the  $\Delta V_T$  as a function of the number of programming pulses described in Fig. S2 of Supplementary Information.

With both the extracted  $w_G^{fd}$  and  $w_G^{ff}$ , the  $dr_w$  can be calculated as approximately 2.2 in the above-threshold and 47 in the sub-threshold regime, respectively, using Eq. (16). As expected, the  $dr_w(sub)$  is much bigger compared to the  $dr_w(above)$ . This is because the exponential function of the sub-threshold regime (see Eq. 18) can be more sensitive compared to the rational function of the above-threshold regime (see Eq. 17) as the  $\Delta V_T^{fd}$  for each operating region is comparable (see Fig. 3). Accordingly, for the sub-threshold regime, it is verified that the  $dr_w$  is much larger compared to the above-threshold regime. Note that, since  $dr_w(sub) > dr_w(above)$  with the same pulse width for each synaptic process with respect to two operating regions, it can be considered that the speed of weight updates in the sub-threshold regime is faster compared to the above-threshold regime.

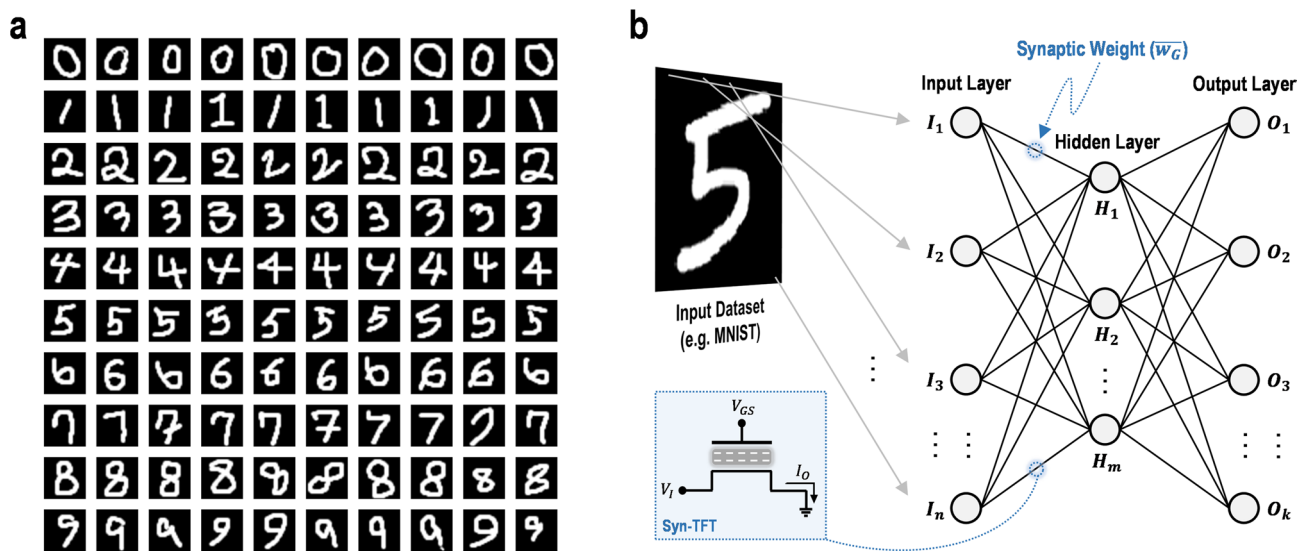
Additionally, as another observation of the pulsed characteristics, the  $P_{static}^{read}$  can be estimated during the synaptic processes for two operational regimes, as seen in Fig. 4g,h. It is found that the  $P_{static}^{read}$  in each operating region decays for the synaptic depression since the  $I_O$  is reduced with applying positive programming pulses, which can be explained with Eq. (19). After the synaptic depression, with applying a series of negative programming pulses, it is shown that the  $P_{static}^{read}$  is gradually increased for the synaptic facilitation due to the gradual increase of the  $I_O$ , which is consistent with Eq. (19). Afterward, at the FF, the  $P_{static}^{read}$  finally arrives at the  $P_{static}^{read(max)}$  of 9.1 nW in the above-threshold regime and 0.28 nW in the sub-threshold regime, respectively, which can be calculated with Eq. (20). As can be seen, the  $P_{static}^{read(max)}$  in the sub-threshold regime is found to be about two orders of magnitude smaller than that in the above-threshold regime since the operating current in the sub-threshold regime is much lower compared to the above-threshold regime, as indicated in Fig. 3a,b.

**Weight linearity of the Syn-TFT.** As another observation for the characteristics of weight updates (e.g. the  $dr_w$ ), the weight linearity can be checked for two operating regions, using the plots of  $\Delta\overline{w}_G$  versus  $\overline{w}_G$  based on the extracted data in Fig. 4e,f. Here, the  $\Delta\overline{w}_G$  is represented as a difference between the  $w_G^n$  and  $w_G^{n-1}$  where the  $w_G^n$  is the  $\overline{w}_G$  after applying the  $n$ th programming pulses and  $w_G^{n-1}$  is the  $\overline{w}_G$  before applying the  $n$ th programming pulse. Figure 5 describes the weight updates of the Syn-TFT with a color map of the cumulative distribution function (CDF) for the synaptic depression and facilitation of those operating regions, respectively, applying the noise components (e.g. the noise power spectral density (NPSD)). Here, the CDF distribution presents a probabilistic variation of the  $\Delta\overline{w}_G$  with respect to a synaptic state. In addition, with the plots of  $\Delta\overline{w}_G$  versus  $\overline{w}_G$ , the linear range in the weight update is estimated with 30% variation of the  $\Delta\overline{w}_G$ . As indicated in Fig. 5a,b, it is shown that the linear range of the weight for the synaptic depression (i.e.  $\mathcal{L}_d$ ) in the above-thresh-



Parameters	Above-threshold regime	Sub-threshold regime
$\Delta V_T^{\text{fd}}$ (V)	0.46	0.58
$I_O^{\text{ff}}$ (A)	$91 \times 10^{-9}$	$2.8 \times 10^{-9}$
$I_O^{\text{fd}}$ (A)	$41 \times 10^{-9}$	$6.0 \times 10^{-11}$
$dr_w$	2.2	47
$P_{\text{static}}^{\text{read}} (\text{max})$ (W)	$9.1 \times 10^{-9}$	$28 \times 10^{-11}$
$\mathcal{L}_{\text{avg}}$	0.626	0.764

**Table 1.** Summary of extracted parameters for two operating regions of the fabricated Syn-TFT.



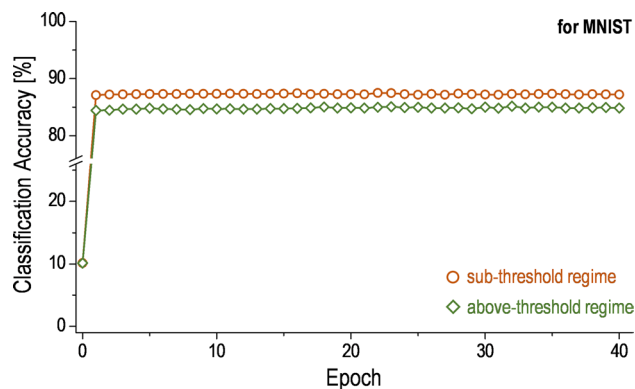
**Figure 6.** (a) Examples of handwritten digits in the MNIST (Modified National Institute of Standards and Technology database) to be classified with an analog accelerator. Here,  $28 \times 28$  grayscale images in the MNIST are used for the training. (b) Schematic of an artificial neural network (ANN) with three layers as a simple multi-layer perceptron (MLP) structure. Here, the  $I_n$  is  $n$ th neuron in an input layer,  $H_m$  is  $m$ th neuron in a hidden layer, and  $O_k$  is  $k$ th neuron in an output layer.

old and sub-threshold regimes is 0.615 and 0.562, respectively. Moreover, the linear range of the weight for the synaptic facilitation (i.e.  $\mathcal{L}_f$ ) is found to be 0.637 in the above-threshold regime and 0.966 in the sub-threshold regime, respectively (see Fig. 5c,d). With both the extracted  $\mathcal{L}_d$  and  $\mathcal{L}_f$ , the arithmetic mean with respect to the linear range of the weight (i.e.  $\mathcal{L}_{\text{avg}} = (\mathcal{L}_d + \mathcal{L}_f)/2$ ) for total synaptic processes can be calculated as 0.626 in the above-threshold regime and 0.764 in the sub-threshold regime, thus a good linearity with a wider linear range of the weight in the sub-threshold regime. This can be because a large  $dr_w$  as a ratio between the  $w_G^{\text{fd}}$  and  $w_G^{\text{ff}}$  leads to the wide linear range of the weight. In addition, this result is consistent with a linear increase of  $\overline{w_G}$  for the synaptic facilitation in the sub-threshold regime, as shown in Fig. 4f. Note that the physical interpretation with respect to the weight linearity is specifically explained with  $I_O$  versus  $\Delta V_T$  and  $\Delta V_T$  versus time ( $t$ ) plots for two operating regions in Supplementary Information (see Fig. S1a–d in Section S1).

Therefore, it is verified that the weight linearity in the sub-threshold regime can be better compared to the above-threshold regime for overall synaptic processes (e.g. the synaptic depression and facilitation), achieving the wide linear range of the weight.

**Analog accelerator based on the Syn-TFT Array.** As shown in the previous sections, for the device level, the  $dr_w(\text{sub})$  is found to be larger compared to the  $dr_w(\text{above})$ , showing a relatively lower power consumption. And it is also found that the linear range of the weight in the sub-threshold regime is wider compared to the above-threshold regime. These results are summarized in Table 1 for two operating regions.

With these results indicated in Table 1, it can be expected that performances, such as the CA, of the AA based on the Syn-TFT array are to be different in those operating regions at the array level. To check this, the CA as a result of the classification task needs to be monitored. Note that handwritten digits in the Modified National Institute of Standards and Technology database (MNIST) are used for the classification task of the AA, as shown in Fig. 6a. To evaluate the performance of the AA based on the Syn-TFT array for an artificial neural network (ANN), the 60,000 training examples and 10,000 test examples are also used (see Fig. 6b)<sup>35</sup>. In addition, the network size, which can be calculated as the product of the size of each layer in the ANN (e.g. the input, hidden, and output layers), is  $784 \times 300 \times 10$  for the MNIST classification. Note that, in order to resolve the effects of the



**Figure 7.** Classification accuracy (CA) of the analog accelerator (AA) based on the Syn-TFT array versus training epochs for the above-threshold regime and sub-threshold regime, respectively. Here, the classification task is performed with the CrossSim™ and tested for the MNIST.

$dr_w$  and linearity on the AA performance with respect to two operating regions, the effect of the signal-to-noise ratio (SNR) is intentionally kept the same for each operating region. So, the SNR is equally set in the simulation while considering noise components with 10% of signal for two cases<sup>39</sup>. As a result of the classification task with this AA, the CA is estimated in Fig. 7 for two operating regions, which is tested for 40 epochs. As can be seen, the CA of the sub-threshold regime is found to be bigger compared to the above-threshold regime at each epoch, resulting in the maximum value of 87.51%. This can be explained with the  $dr_w$  and  $\mathcal{L}_{avg}$  for two operating regions. For the case of the  $dr_w$ , it is found that the sub-threshold regime is larger than that of the above-threshold regime, as shown in Table 1. Moreover, the  $\mathcal{L}_{avg}$  of the sub-threshold regime is found to be wider compared to the above-threshold regime (see Table 1), thus a relatively good weight linearity in the sub-threshold regime. Here, since the SNR, which is proportional to the CA, is intentionally set the same for both two operating regions, it is expected that other parameters, such as the dynamic ratio and weight linearity, would mainly determine the CA. Note that the comparison related to efficiency metrics between proposed Syn-TFT and existing similar synaptic devices is shown in Supplementary Information (see Section S3)<sup>40–43</sup>.

Consequently, these results indicate that the Syn-TFT operated in the sub-threshold regime can achieve a relatively large  $dr_w(sub)$  while showing a relatively wide linear range of the weight. With these results of the device level, when the Syn-TFT array is used as the AA, it is also found that the CA of the sub-threshold regime is larger than that of the above-threshold due to those advantages at the device level of the Syn-TFT operated in the sub-threshold region. Therefore, it is believed that the selection of the sub-threshold regime in the Syn-TFT is essential for a high performance of the ANN while achieving a large  $dr_w$  and good weight linearity.

## Conclusion

In this article, an experimental framework on the characteristics of operating region-dependent weight updates for the Syn-TFT with an amorphous IGZO channel layer has been shown. To implement a memory function of the IGZO TFT, the defective oxides, such as SiO<sub>2</sub>, have been intentionally used for a charge trapping or de-trapping because of programming pulses to the gate terminal. Based on this memory function, a conductance of the Syn-TFT is changed, which is dependent on the programming pulses, thus a weight updates. This weight updates characteristics has been analyzed for the above-threshold and sub-threshold regimes in terms of the dynamic ratio, respectively. Here, the operating region has been selected depending on the level of the gate read-voltage relative to the threshold voltage of the Syn-TFT. In order to verify these, a static and pulsed characteristics of the fabricated Syn-TFT have been experimentally monitored with a standard semiconductor analyzer. From the experimental results, it has been shown that the dynamic ratio of the sub-threshold regime ( $\approx 47$ ) is larger compared to the above-threshold regime ( $\approx 2.2$ ) and the weight linearity in the sub-threshold regime is better compared to the above-threshold regime. Since either the dynamic ratio or weight linearity is expected to affect performances (e.g. CA) of the AA based on the Syn-TFT array, to check this, the AA simulation has been performed with a crossbar simulator. From the simulation result, it has been found that the CA of the sub-threshold regime is always larger compared to the above-threshold regime at each epoch, indicating the maximum value of 87.51% due to the advantages in the device level of the Syn-TFT operated in the sub-threshold region (e.g. a relatively large dynamic ratio and linear range). Consequently, it can be believed that the proposed Syn-TFT device with both a large dynamic ratio and good linearity for the sub-threshold regime would be essential for the neuromorphic system.

## Data availability

All data generated or analyzed during this study are included in this published article.

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## References

- Indiveri, G. & Liu, S. C. Memory and information processing in neuromorphic systems. *Proc. IEEE* **103**(8), 1379–1397 (2015).
- Upadhyay, N. K. *et al.* Emerging memory devices for neuromorphic computing. *Adv. Mater. Technol.* **4**(4), 1800589 (2019).
- Li, H. *et al.* A light-stimulated synaptic transistor with synaptic plasticity and memory functions based on InGaZnO<sub>x</sub>-Al<sub>2</sub>O<sub>3</sub> thin film structure. *J. Appl. Phys.* **119**(24), 244505 (2016).
- Kang, Y., Jang, J., Cha, D. & Lee, S. Synaptic weight evolution and charge trapping mechanisms in a synaptic pass-transistor operation with a direct potential output. *IEEE Trans. Neural Netw. Learn. Syst.* **32**(10), 4728–4741 (2021).
- Yu, S. *et al.* An electronic synapse device based on metal oxide resistive switching memory for neuromorphic computation. *IEEE Trans. Electron Dev.* **58**(8), 2729–2737 (2011).
- Dai, S. *et al.* Light-stimulated synaptic devices utilizing interfacial effect of organic field-effect transistors. *ACS Appl. Mater. Interfaces*. **10**(25), 21472–21480 (2018).
- Park, Y. & Lee, J. Artificial synapses with short- and long-term memory for spiking neural networks based on renewable materials. *ACS Nano* **11**(9), 8962–8969 (2017).
- Yang, R. *et al.* Synaptic plasticity and memory functions achieved in a WO<sub>3</sub>-x-based nanoionics device by using the principle of atomic switch operation. *Nanotechnology* **24**(38), 384003 (2013).
- Xu, W. *et al.* Organic core-sheath nanowire artificial synapses with femtojoule energy consumption. *Sci. Adv.* **2**(6), e1501326 (2016).
- Eryilmaz, S. *et al.* Brain-like associative learning using a nanoscale non-volatile phase change synaptic device array. *Front. Neurosci.* **8**, 205 (2014).
- Park, S. *et al.* Effect of the gate dielectric layer of flexible InGaZnO synaptic thin-film transistors on learning behavior. *ACS Appl. Electron. Mater.* **3**(9), 3972–3979 (2021).
- Peng, C. *et al.* Photoelectric IGZO electric-double-layer transparent artificial synapses for emotional state simulation. *ACS Appl. Electron. Mater.* **1**(11), 2406–2414 (2019).
- Zhu, L. *et al.* Synergistic modulation of synaptic plasticity in IGZO-based photoelectric neuromorphic TFTs. *IEEE Trans. Electron Dev.* **68**(4), 1659–1663 (2021).
- Duan, N. *et al.* An electro-photo-sensitive synaptic transistor for edge neuromorphic visual systems. *Nanoscale* **11**(38), 17590–17599 (2019).
- Wu, Q. *et al.* Photoelectric plasticity in oxide thin film transistors with tunable synaptic functions. *Adv. Electron. Mater.* **4**(12), 1800556 (2018).
- Jang, J. *et al.* Thin-film optical devices based on transparent conducting oxides: Physical mechanisms and applications. *Curr. Comput.-Aided Drug Des.* **9**(4), 192 (2019).
- Bae, J., Jeong, I. & Lee, S. Wavelength-dependent optical instability mechanisms and decay kinetics in amorphous oxide thin-film devices. *Sci. Rep.* **9**(1), 1–6 (2019).
- Yang, P. *et al.* Synaptic transistor with a reversible and analog conductance modulation using a Pt/HfO<sub>x</sub>/n-IGZO memcapacitor. *Nanotechnology* **28**(22), 225201 (2017).
- Huang, W. *et al.* Memristive artificial synapses for neuromorphic computing. *Nano-Micro Lett.* **13**(1), 1–28 (2021).
- Kwon, S. *et al.* Environment-adaptable artificial visual perception behaviors using a light-adjustable optoelectronic neuromorphic device array. *Adv. Mater.* **31**(52), 1906433 (2019).
- Jang, Y. *et al.* Amorphous InGaZnO (a-IGZO) synaptic transistor for neuromorphic computing. *ACS Appl. Electron. Mater.* **4**(4), 1427–1448 (2022).
- Subramanian Periyal, S. *et al.* Halide perovskite quantum dots photosensitized-amorphous oxide transistors for multimodal synapses. *Adv. Mater. Technol.* **5**(11), 2000514 (2020).
- Park, Y., Kim, M. & Lee, J.-S. Artificial synaptic transistors based on Schottky barrier height modulation using reduced graphene oxides. *Carbon* **165**, 455–460 (2020).
- Beom, K. *et al.* Single- and double-gate synaptic transistor with TaO<sub>x</sub> gate insulator and IGZO channel layer. *Nanotechnology* **30**(2), 025203 (2018).
- Daus, A. *et al.* Ferroelectric-like charge trapping thin-film transistors and their evaluation as memories and synaptic devices. *Adv. Electron. Mater.* **3**(12), 1700309 (2017).
- Yang, P. *et al.* Synaptic behaviors of thin-film transistor with a Pt/HfO<sub>x</sub>/n-type indium–gallium–zinc oxide gate stack. *Nanotechnology* **29**(29), 295201 (2018).
- Lee, S. & Nathan, A. Subthreshold Schottky-barrier thin-film transistors with ultralow power and high intrinsic gain. *Science* **354**(6310), 302–304 (2016).
- Duan, H. *et al.* IGZO/CsPbBr<sub>3</sub> nanoparticles/IGZO neuromorphic phototransistors and their optoelectronic coupling applications. *ACS Appl. Mater. Interfaces*. **13**(25), 30165–30173 (2021).
- Kiebel, S. J., Daunizeau, J. & Friston, K. J. A hierarchy of time-scales and the brain. *PLoS Comput. Biol.* **4**(11), e1000209 (2008).
- Kuzum, D., Yu, S. & Wong, H. P. Synaptic electronics: Materials, devices and applications. *Nanotechnology* **24**(38), 382001 (2013).
- Hoshino, K. *et al.* Constant-voltage-bias stress testing of a-IGZO thin-film transistors. *IEEE Trans. Electron Dev.* **56**(7), 1365–1370 (2009).
- Lee, S. Bias-dependent subthreshold characteristics and interface states in disordered semiconductor thin-film transistors. *Semicond. Sci. Technol.* **34**(11), 1101 (2019).
- Hayt, W., Kemmerly, J. & Durbin, S. *Engineering Circuit Analysis* (McGraw-Hill, 1978).
- Agarwal, S. *et al.* *Hughart. CrossSim*. <http://crosssim.sandia.gov> (2018).
- Agarwal, S. *et al.* Resistive memory device requirements for a neural algorithm accelerator. in *2016 International Joint Conference on Neural Networks (IJCNN)* (2016).
- Cox, J., Conrad, D. J. & James, B. A. A signal processing approach for cyber data classification with deep neural networks. *Proc. Comput. Sci.* **61**, 349–354 (2015).
- Cha, D., Kang, Y., Lee, S. & Lee, S. A geometrical optimization rule of the synaptic pass-transistor for a low power analog accelerator. *IEEE Access* **10**, 35120–35130 (2022).
- Lee, S. A gate bias and temperature dependencies of contact resistances in amorphous oxide semiconductor thin-film transistors. *IEEE Access* **9**, 165085–165089 (2021).
- Razavi, B. *Design of Analog CMOS Integrated Circuits* (Tata McGraw-Hill Education, 2002).
- Rao, J. *et al.* An electroforming-free, analog interface-type memristor based on a SrFeO<sub>x</sub> epitaxial heterojunction for neuromorphic computing. *Mater. Today Phys.* **18**, 100392 (2021).
- Tang, J. *et al.* ECRAM as scalable synaptic cell for high-speed, low-power neuromorphic computing. in *2018 IEEE International Electron Devices Meeting (IEDM)* (2018).
- Mohta, N. *et al.* An artificial synaptic transistor using an α-In<sub>2</sub>Se<sub>3</sub> van der Waals ferroelectric channel for pattern recognition. *RSC Adv.* **11**(58), 36901–36912 (2021).
- Lee, Y. *et al.* IGZO synaptic thin-film transistors with embedded AlO<sub>x</sub> charge-trapping layers. *Appl. Phys. Exp.* **15**(6), 061005 (2022).

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## Author contributions

D.C. and Y.K. performed the measurement and characterization with the fabricated devices. D.C. did the synaptic analysis with Y.K. Y.K. drew figures with D.C. All the authors did the discussion writing the manuscript together.

## Competing interests

The authors declare no competing interests.

## Additional information

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