

## ARTICLE OPEN



# Error per single-qubit gate below $10^{-4}$ in a superconducting qubit

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Implementing arbitrary single-qubit gates with near perfect fidelity is among the most fundamental requirements in gate-based quantum information processing. In this work, we fabricate a transmon qubit with long coherence times and demonstrate single-qubit gates with the average gate error below  $10^{-4}$ , i.e.  $(7.42 \pm 0.04) \times 10^{-5}$  by randomized benchmarking (RB). To understand the error sources, we experimentally obtain an error budget, consisting of the decoherence errors lower bounded by  $(4.62 \pm 0.04) \times 10^{-5}$  and the leakage rate per gate of  $(1.16 \pm 0.04) \times 10^{-5}$ . Moreover, we reconstruct the process matrices for the single-qubit gates by the gate set tomography (GST), with which we simulate RB sequences and obtain single-qubit fidelities consistent with experimental results. We also observe non-Markovian behavior in the experiment of long-sequence GST, which may provide guidance for further calibration. The demonstration extends the upper limit that the average fidelity of single-qubit gates can reach in a transmon-qubit system, and thus can be an essential step towards practical and reliable quantum computation in the near future.

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## INTRODUCTION

Reliability is an unavoidable crux in the quest for beyond-classical computational capabilities. As to the circuit-based quantum computation, improving the reliability of entire computational tasks is decomposed into a series of subtasks, among which implementing high-fidelity single-qubit gates is an important component. For example, both the noisy intermediate-scale quantum (NISQ) application and the fault-tolerant quantum computation, i.e. the near-term and the ultimate goals of the circuit-based quantum computation, make requirements on gate fidelities that exceed the state-of-the-art values. As a result, considerable efforts have been invested in realizing high-fidelity single qubit gates in leading platforms for quantum information processing, such as the trapped-ion and neutral atoms systems<sup>1,2</sup>. As to the superconducting quantum computation, great progress has been made over the past two decades, including the realization of accurate and precise quantum gates. The single- and two-qubit gate errors in transmon qubit are below  $10^{-3}$  and  $10^{-23-10}$  respectively, and the single-qubit gate error in fluxonium qubit is below  $10^{-4}$  owing to a millisecond coherence time<sup>10</sup>. To further improve the single-qubit gate fidelity, identifying the nature of the dominant errors is particularly important for improving performance and the fidelity of single-qubit can be seen as the upper boundary of two-qubit gate.

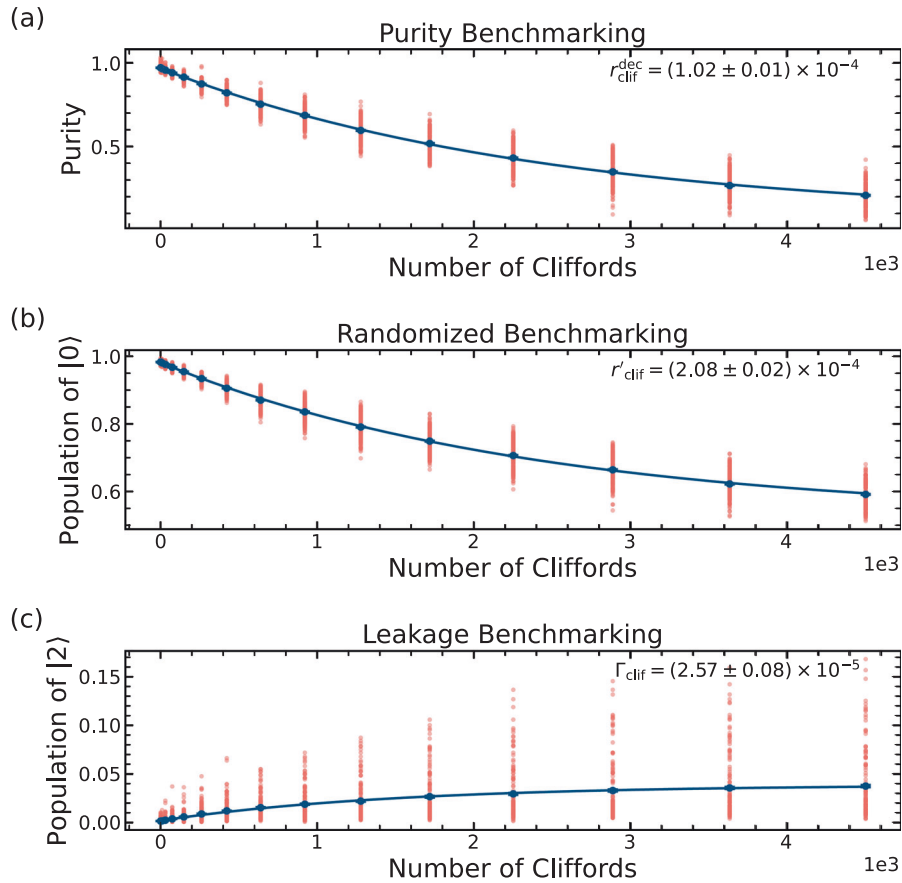
One of the essential requirements for the reliable implementation of circuit-based quantum computation is a sufficiently large ratio between the coherence time and the gate length. To increase this ratio, one prevalent way is to increase the coherence times of the quantum devices, including the energy relaxation time and the dephasing time. As to the superconducting transmon system, the detrimental impact of the two-level-system (TLS) defects in dielectrics on the energy-relaxation time has been extensively studied<sup>11-13</sup>. To relieve this impact, two alternative directions have been explored, one is to suppress the

coupling between the TLS defects and the transmon by optimizing the geometry<sup>14</sup>, and the other is to lower the density of the TLS defects by appropriate materials and recipes<sup>15,16</sup>. On the other hand, environmental noises, such as the fluctuation of magnetic flux and the residue of the thermal photons, can cause qubit dephasing and thus decrease the dephasing time<sup>17-20</sup>. The solution might be to keep the transmon-qubit well separated from its environment, e.g. mitigating its couplings to the drive lines, the readout resonator or the neighboring qubits. However, this is obviously in contradiction to the implementation of fast qubit control, which requires the qubit apt to be driven. As a result, balancing the coherence times and the gate length becomes particularly important for the implementation of high-fidelity single-qubit gates. Besides, to mitigate the impact of the spurious reflection signal due to impedance mismatch in the line, a short buffer should be added after each single-qubit operations to avoid residual pulse overlap<sup>21</sup>.

In this work, we design and fabricate a superconducting device consisting of superconducting transmons, which are of long coherence times and apt to strong drivings. With this device, we implement a set of single-qubit gates, constructing from the  $X_{\frac{\pi}{2}}$  gate and the virtual Z gates. The average gate fidelity, as well as the fidelities for  $\pi/2$ -pulses, is benchmarked to be higher than 99.99%, exceeding the state-of-the-art record in superconducting transmon-qubit systems. We also analyse the sources of the residual errors, including the incoherent error the leakage rate per Clifford gate. As a cross validation, we experimentally obtain process matrices for the identity and  $\pi/2$ -pulses with the gate-set tomography. Besides extending the computational upper limit of a transmon-qubit processor, our experiment also indicates that the bottleneck to further increase the reliability might be to suppress the non-Markovian effect.

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**Fig. 2 PB for the single-qubit gate.** **a** The sequence purity averaged over 100 random sequences as a function of the number of Cliffords  $m$  for the reference. **b** The sequence RB (100 averages) is a function of the number of Cliffords  $m$  for the reference. **c**  $|2\rangle$  state population versus the gate numbers showing accumulation of leakage with gate numbers.

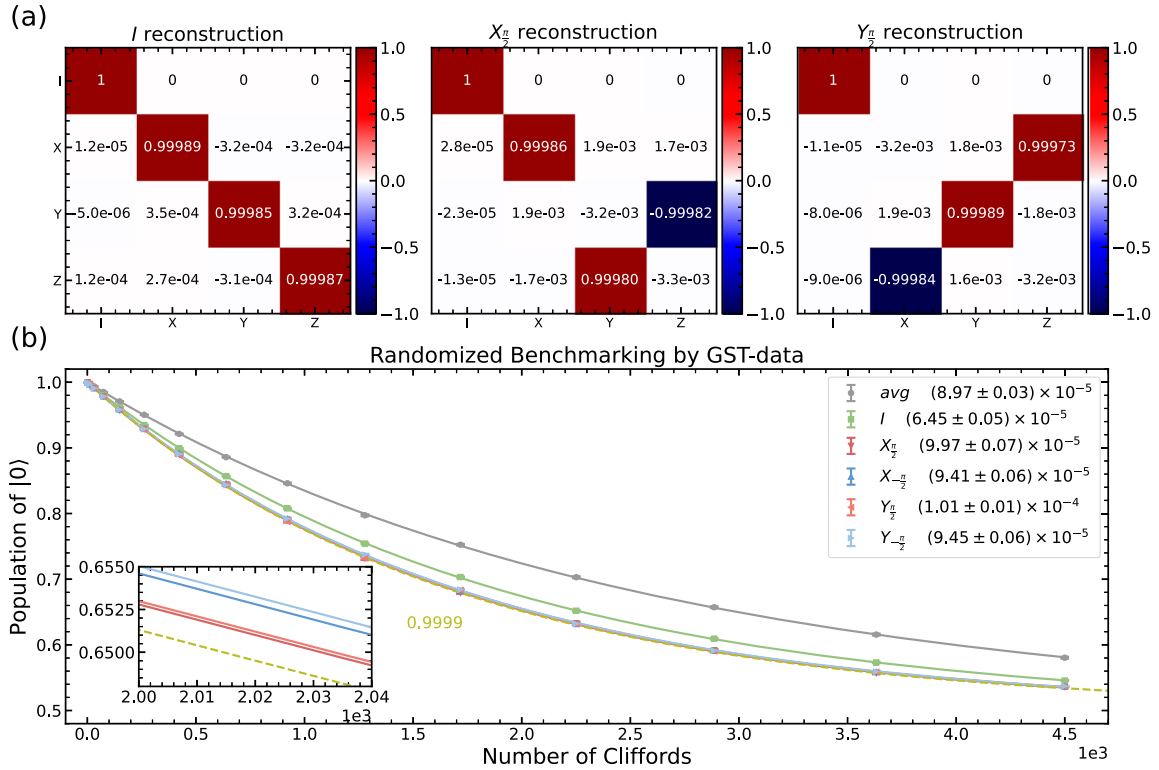
together with the repetition time for each circuit being  $M = 1024$ , it leads to an estimated accuracy of  $\sim \frac{1}{\sqrt{ML}} < 10^{-5}$ . The collected experimental data are analysed by the maximum likelihood estimation, leading to process matrices for the gate set which maximizes the log-likelihood function. The reconstructed process matrices are shown in Fig. 3a. With these matrices, we simulate the gate sequences used in the RB experiment shown in Fig. 1 and obtain the simulated EPG  $r_{\text{avg}}^{\text{sim}} = (8.97 \pm 0.03) \times 10^{-5}$ , as shown in Fig. 3b. Note that these experiments, i.e. RB and GST, are executed in a relatively long period of time. The consistency of these two values shows the stability of our superconducting device, which is capable of implementing high-fidelity quantum gates over a relatively long period of time, while the deviation between them gives an intuitive quantification of the temporal fluctuation, which is on the level of  $\sim 10^{-5}$ .

Besides the estimated process matrices, the GST experiment also reveals the temporal correlation property, i.e. the non-Markovianity, of the physical platform in terms of model violations<sup>27</sup>. The GST models a noisy quantum device with complete positive and trace preserving quantum channels, which are expressed by process matrices. Model violations emerge when there are deviations between the probabilities predicted by the optimal process matrices obtained from the GST and the observed frequencies in experiments, probably due to the existence of time-correlated errors<sup>29</sup>, e.g. the slow drifting of system parameters. In pyGSTi, model violation is quantified by the loglikelihood score, which is defined as the distance between the optimized log-likelihood function and its mean, measured in the unit of its standard deviation, with the statistics given by the  $\chi^2$ -distribution

widely used in hypothesis tests. As to the GST experiment carried in our device, the individual loglikelihood scores (see “Supplementary Fig. 2”) show statistically significant model violation mostly in long sequences with depth  $\geq 256$ . In other words, the non-Markovian effect can be measured on the accuracy level of  $\sim 10^{-4}$ , which is comparable to the average EPG given by the RB experiment.

## DISCUSSION

Although the non-Markovian errors defy accurate and reliable error analysis of the benchmarking results, direct monitoring of the control parameters still provides useful information about the error sources of the experimental platform. We consider two of the possible noise sources, i.e. classical noise from the electronics in the control system and the fluctuation of the transmon frequency, where the former corresponds to the DAC amplitude fluctuation in our experiment. These two fluctuations are monitored to be about 0.3% and 0.1 MHz (see “Supplementary Figs. 3, 4”), of which the contributions to the EPG are estimated to be  $0.2 \times 10^{-5}$  and  $0.1 \times 10^{-5}$ , respectively. Specifically, we numerically simulate the evolution of the Schrödinger equation, with the pulse amplitude or the qubit frequency randomly sampled from normal distributions, whose mean and variance are determined by the target values and the monitored fluctuation strengths. As the estimated contributions to the EPG are much lower than the coherent part of the measured EPG, we conclude that the non-Markovianity is the main obstructive factor to further improve the fidelity.



**Fig. 3 Reconstructing process matrix and the simulation results of RB.** **a** Reconstructing process matrix estimated by GST. The process matrix estimates of the  $I$ ,  $X_{\frac{\pi}{2}}$  and  $Y_{\frac{\pi}{2}}$  gates are shown as superoperators on the basis of Pauli matrices, respectively. **b** RB data simulated using the gate set tomography  $\mathcal{G}_0$  derived from experimental GST results. The reconstructing process matrix generates the single qubit Clifford group. Simulate the RB experiment and analysis the gate fidelity. The inset is the enlarge view of the curves.

## METHODS

### Experiment setup

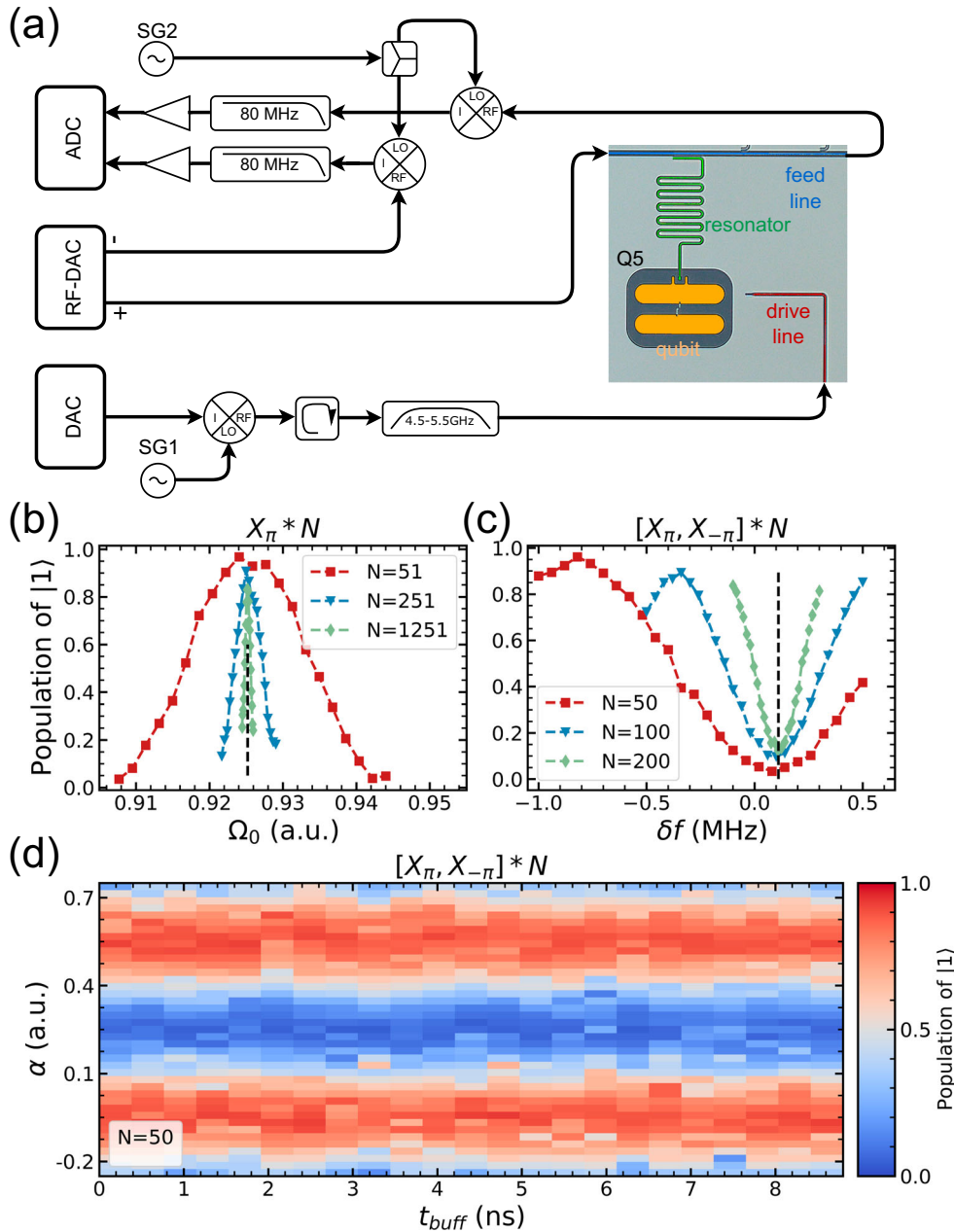
We implement high-fidelity single-qubit gates on a fixed-frequency transmon qubit fabricated with tantalum films<sup>15,16</sup>. As shown in the insert of Fig. 4a, the qubit, labeled by  $Q_5$ , is embedded on a superconducting device consisting of five separate transmon qubits, each of which is coupled to a readout resonator sharing one transmission line. The  $Q_5$  qubit is coupled to a microwave control line, which facilitates fast single-qubit operations. The transition frequency between  $|0\rangle$  and  $|1\rangle$  and the anharmonicity are  $\omega_{01} = 2\pi \times 4.631$  GHz and  $\Delta = -2\pi \times 240$  MHz, respectively. As to the coherence times, the energy relaxation time and the dephasing time are measured to be  $T_1 = 231$   $\mu$ s and  $T_2^* = 204$   $\mu$ s. These long coherence times indicate that the qubit is quite isolated from its external environment. Together with the electronics for the control and measurement system is shown in Fig. 4a and around 50 dB attenuators are assigned in the fridge to mitigate the effects of thermal noise. Single-qubit gates with a gate length of 20 ns are a favorable choice without the extra amplifier to achieve the high-fidelity gate.

Specifically, we use the single-sideband (SSB) technology to modulate the control pulses, where the microwave signals are generated by a Digital-to-Analog Converter (DAC) with sampling rate 5 GS/s, a Signal Generator (SG1) and a mixer. To suppress local leakage and unwanted spurious signals, like the image or reflection of the control signals due to impedance mismatch, a bandpass filter and an isolator are introduced before the control signals access the qubit. The readout pulses are generated by a radio frequency-DAC (RF-DAC) with the sampling rate of 25 GS/s and fed into the fridge from the positive (+) port, while an out-of-phase signal from the negative (-) port is used as reference. Finally, the down-converted signals, going out of the fridge, are

collected by an Analog-to-Digital Converter (ADC). The qubit is coupled to a readout resonator and the first three states  $|0\rangle$ ,  $|1\rangle$ , and  $|2\rangle$  can be distinguished with high fidelity. The details on the readout can be found in "Supplementary Section I".

### Pulse parameter calibration

An arbitrary rotation in the Bloch sphere can be decomposed to rotate over the x (y) and z-axis according to Euler decomposition. In this experiment, we construct arbitrary single-qubit rotations with  $X_{\frac{\pi}{2}}$  pulses and virtual Z gates<sup>30</sup>. As the virtual Z gates can be treated as faultless,  $X_{\frac{\pi}{2}}$  is the only gate that needs precise calibration. The details on the pulse decomposition can be found in Supplement section IV. Here we implement the  $X_{\frac{\pi}{2}}$  gate by a microwave pulse with a cosine-shaped envelope  $\Omega(t) = \Omega_0(1 - \cos(2\pi t/t_g))$  with the gate length  $t_g = 20$  ns. To suppress leakage and phase errors, we introduce the derivative reduction by adiabatic gate (DRAG) scheme<sup>31–33</sup> for the pulse envelope, i.e.  $\Omega_{\text{DRAG}}(t) = e^{i2\pi\delta f t}(\Omega(t) - i\alpha \frac{\Omega(t)}{\Delta})$  with  $\Delta$  being the anharmonicity of the transmon qubit, where  $\alpha$  and  $\delta f$  are the DRAG weighting and detuning, respectively. Considering the fact that long periodic sequences can boost the sensitivity of certain coherent errors, we design and implement long-sequence calibration schemes to determine the exact values of the pulse amplitude  $\Omega_0$  and the DRAG detuning  $\delta f$ . To be specific,  $\Omega_0$  is calibrated with  $N$  times of  $X_{\pi}$  pulses, each of which is composed of two  $X_{\frac{\pi}{2}}$  pulses. With  $N$  being a large and odd number, we sweep the value of  $\Omega_0$ , with the optimum marked by a peak in the population of the  $|1\rangle$  state, denoted as  $P_1$ . The calibration results are shown in Fig. 4b. The periodic calibration sequences for  $\delta f$  consist of  $N$  pseudo-identity operators<sup>34</sup>, each of which is composed of a composite  $X_{\pi}$  pulse and its inverse gate, i.e. the  $X_{-\pi}$  gate. Through the repeated application of the pseudo-identity gate, the phase error originating from the high-level effect of the AC Stark shift is amplified.



**Fig. 4 Overview of the experiment setup and pulse calibration.** **a** The optical image of the qubit device and the schematic of the experiment setup. **b** Calibration of the pulse amplitude  $\Omega_0$  for the  $X_{\pi/2}$  gate. The inset shows the pseudo-identity sequence. **c** Optimize the detuning of the pulse. The pseudo-identity sequence is shown in the inset. The curves show the excited state population as a function of detuning  $\delta f$  for  $N = 50, 100, 200$  pairs. **d**  $X_{\pi}$  and  $X_{-\pi}$  gate sequence to check the trailing edge of the pulse. There is no population change along the duration of the buffer  $t_{\text{buff}}$  of the pseudo-identity sequence.

This phase error can be compensated for by introducing the frequency detuning  $\delta f$  during pulse modulation. As shown in Fig. 4c, the minimum in  $P_1$  when sweeping  $\delta f$  gives the optimum of  $\delta f$ . The experimental results clearly demonstrate the increasing sensitivity as the sequence length, which is  $\propto N$ , increases. The final step is to determine the length of the buffer by what time the spurious signals can be neglected. In order to shorten the buffer time, we measure the trailing edge of the driving pulse by directly using the qubit<sup>35</sup> and then correct the signal by pre-distortion. The efficacy of this technique is reflected in the experimental data shown in Fig. 4d. Starting from the  $|0\rangle$  state, we implement a sequence consisting of 50 pairs of  $(X_{\pi}, X_{-\pi})$  pulses and measure

the population in the  $|1\rangle$  state. We collect experimental data by sweeping the duration of the buffer and the DRAG weighting parameter  $\alpha$ , with the variation of the latter providing an oscillating pattern. Although we observe no visible movement of the pattern as the buffer time increases, we still chose  $t_{\text{buff}} = 2$  ns to guarantee there is no overlap between pulses, i.e. the gate length of a single  $X_{\pi/2}$  gate is 22 ns.

#### DATA AVAILABILITY

All data needed to evaluate the conclusions in the paper are present in the main text and/or the Supplementary Information. Additional data related to this paper may be requested from the authors.

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## AUTHOR CONTRIBUTIONS

The project was conceived by H.Y., Y.J. and W.L. The measurements were performed by Z.L., P.L., H.X. and W.L. The experimental data were analyzed by Z.L., P.L., J.Z., P.Z. and W.L. The devices are designed by G.X. and Z.M. The devices are fabricated by T.S., Z.M., X.L., W.S. W.L., J.Z. and H.Y. wrote the manuscript. All authors discussed the results and the manuscript.

## COMPETING INTERESTS

The authors declare no competing interests.

## ADDITIONAL INFORMATION

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