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OPEN Electro-thermal control of aluminum-doped zinc oxide/ vanadium dioxide multilayered thin films for smart-device applications

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We demonstrate the electro-thermal control of aluminum-doped zinc oxide (Al:ZnO) /vanadium dioxide (VO₂) multilayered thin films, where the application of a small electric field enables precise control of the applied heat to the VO₂ thin film to induce its semiconductor-metal transition (SMT). The transparent conducting oxide nature of the top AI:ZnO film can be tuned to facilitate the fine control of the SMT of the VO₂ thin film and its associated properties. In addition, the AI:ZnO film provides a capping layer to the VO₂ thin film, which inhibits oxidation to a more energetically favorable and stable V₂O₅ phase. It also decreases the SMT of the VO₂ thin film by approximately 5–10 °C because of an additional stress induced on the VO₂ thin film and/or an alteration of the oxygen vacancy concentration in the VO₂ thin film. These results have significant impacts on technological applications for both passive and active devices by exploiting this near-room-temperature SMT.

Vanadium dioxide (VO₂) in thin-film and nanostructured forms has been intensely studied in recent years because of the presence of a metal-insulator (MIT) or semiconductor-metal transition (SMT) near room temperature (RT) at ~68 °C in bulk form, which is also accompanied by a structural phase transition¹⁻⁷. The crystal symmetry of VO₂ changes from a $P2_1/c$ (monoclinic semiconducting phase) to a $P4_2/mnm$ (rutile metallic phase) space symmetry when VO₂ traverses from below to above the SMT. This transition can be temperature-¹, voltage-8-10, and photo-induced11 in this strongly electron-correlated material. The successful growth and study of VO₂ thin films has been demonstrated using several deposition techniques, including reactive sputtering¹², atomic layer deposition (ALD)¹³, pulsed laser deposition (PLD)¹⁴, chemical vapor deposition¹⁵, electron beam evaporation¹⁶, the sol-gel process¹⁷, and thermal evaporation¹⁸. These and many other studies have provided a better understanding of the SMT and VO2 properties; the oxygen vacancy concentration¹⁹ and strain²⁰ play important roles, but many aspects remain unclear. A more thorough understanding of the SMT and VO2 properties will aid in the fine-tuning and control of the SMT so that it can be more reliably used for applications.

Technological applications in various passive and active devices have been developed based on this near-RT SMT. The most notable applications include low-loss plasmonics²¹, smart window coatings²², ultrafast optical switches and sensors¹¹, new electronic devices such as Mott field effect transistors²³, and uncooled bolometers for infrared imaging²⁴. Smart devices, which use the large optical and electrical property changes of VO₂, appear to be at the forefront of this field and are of particular interest here. For example, a smart energy-efficient window must satisfy a number of criteria, including high transmittance in the visible range (400-700 nm), low transmittance in the infrared range $(3-50\,\mu\text{m})$, and variable transmittance in the near-infrared range $(700-3,000\,\text{nm})$. This variable transmittance depends on whether the building interior must be heated or cooled, i.e., the window transmits (reflects) the near-infrared light if there is a heating (cooling) demand²⁵, and it must be actively or passively modulated. In this paper, we discuss a possible thin-film platform in which to electro-thermally control the VO_2 phase across its SMT, which enables modulation for smart window thin-film coatings and/or other technological devices.

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Figure 1. (a) Symmetric θ - 2θ and (b) asymmetric ϕ scans of the multilayered thin-film samples revealing the epitaxial relationship between the film layers and substrate. (c) Schematic and (d) cross-sectional SEM image of the multilayered thin-film samples composed of Au/Ti electrodes on Al:ZnO (370 nm)/VO₂ (50 nm)/Al₂O₃ (0001) substrate.

Results

Epitaxial VO₂ thin films were grown on c-plane (0001) sapphire (Al₂O₃) substrates using oxygen plasma-assisted pulsed laser deposition (PA-PLD). The SMT occurs near 50 °C, as confirmed with Raman spectroscopy and four-point probe electrical measurements, which are not shown here but can be found elsewhere in the literature²⁶. The near-RT SMT of these VO₂ thin films can be induced using the aforementioned techniques, most of which are external to the VO₂ thin film and device. For example, the SMT is typically thermally induced using an external heater in contact with the VO₂. However, the direct incorporation of a thin-film heater into a multilay-ered thin-film device has spatial and temporal advantages, low power requirements, and fast response times^{27,28}. Therefore, a thick film of Al:ZnO, which has unique transparent heater qualities²⁹, was grown on top of the VO₂ thin films using atomic layer deposition (ALD). Details on these Al:ZnO films can be found in the literature^{29–31}. The ALD-grown Al:ZnO film is polycrystalline and the VO₂ thin film has an epitaxial relationship with the sapphire substrate of (010)[100]VO₂ || (0001)[10-10]Al₂O₃ as confirmed via symmetric [Fig. 1(a)] and asymmetric [Fig. 1(b)] x-ray diffraction (XRD) scans.

Electrical contacts were fabricated on top of the Al:ZnO film along opposite edges of the $10 \,\text{mm} \times 10 \,\text{mm}$ sample [see the multilayered thin-film sample schematic in Fig. 1(c)], such that the transparent heating properties of the Al:ZnO film could be used to induce the SMT in the underlying VO₂ thin film. In addition to their transparency, the Al:ZnO films (with an Al:Zn ALD cycle ratio of 1:20) have a moderate thermal conductivity (4.2-4.3 W m⁻¹ K⁻¹), which facilitates heat transport to the VO₂ thin film²⁹. An infrared (IR) camera was used to measure the temperature distribution over the 1 cm² area of the Al:ZnO/VO₂ multilayered thin-film samples at different applied voltages, and these measurements were corroborated via thermocouple measurements. Figure 2 shows the steady-state IR thermal images of the multilayered thin-film samples at applied voltages of 1-5 V. These steady-state temperatures were achieved within 2-5 minutes after applying the voltages, where the time rates of temperature increase are shown in Fig. 3(a); the final steady-state temperatures are plotted versus the applied voltage in Fig. 3(b). The temperature was measured at the center of the samples, where there is a fairly uniform temperature distribution across the entire surface, particularly at or below the SMT. Figure 4 shows this temperature gradient across the multilayered thin-film samples, which was measured between the electrodes from the upper left to the lower right [see Figs 1(c) and 2]. The SMT in these multilayered thin-film samples occurs at just under 3 V (discussed later), where the temperature gradient is $\pm 1 \,^{\circ}$ C across the sample. Smaller temperature gradients are expected by improving the electrodes because of the noticeable defects in the upper and lower left corners of the sample (see Fig. 2), which cause the temperature to decrease at the 0 mm end (see Fig. 4).

The Al:ZnO film is heated because of the Joule heating effect, where the power converted to heat (and consequently the steady-state temperature) is proportional to the square of the applied voltage as shown in Fig. 3(b)⁹. This Joule heating effect can be reproducibly controlled using the Al:ZnO film properties, particularly the film





thickness and amount of Al-doping (i.e., Al:Zn ALD cycle ratio), which govern the metallic behavior of the films based on the carrier concentration as we recently demonstrated²⁹. These two parameters enable one to precisely tailor the amount of heat and temperature, where both thicker and more metallic-like films produce higher temperatures at lower applied voltages, an approach that has considerable advantages in device fabrication and operation.

The SMT of the VO₂ thin film below the Al:ZnO film layer was tracked using various characterization techniques. Micro-Raman spectroscopy is suitable to distinguish the SMT in VO₂. Below the transition, sharp Raman peaks (modes) are observed, which signifies the monoclinic semiconducting phase, whereas only a broadband emission is observed for the rutile metallic phase above the transition^{26,32}. Many, but not all, of the possible Raman modes for the monoclinic semiconducting phase of VO₂ were resolved in the RT spectra^{26,32–35}. The inset of Fig. 5 shows the Raman spectra, which highlights the 196 and 224 cm⁻¹ modes, for both VO₂ phases: the semiconducting phase below the transition and the metallic phase above the transition. These two Raman modes were used to track the SMT in the VO₂ thin film to determine the transition temperature of the Al:ZnO/VO₂ device, which was found to be between 42 and 46 °C (see Fig. 5). Similar and corroborating results were obtained using an external heater below the sapphire substrate to increase the temperature of the VO₂ thin film (not shown).

XRD was also used to track the SMT in the VO₂ thin film as shown in Fig. 6 when voltages were applied to the Al: ZnO/VO_2 multilayered thin-film device. The XRD results show that there is a sudden shift in the VO₂ unit cell parameters at an SMT temperature of 43.5 °C, thus corroborating the Raman spectroscopy results. Below this



Figure 3. (a) Temperature versus time plots for the applied voltages in Fig. 2, where the steady-state final temperatures are achieved within 2–5 minutes. (b) Steady-state temperature versus the applied voltage, which shows the expected parabolic relationship between temperature and applied voltage. Measurements were taken with an IR thermal camera using the temperature at the center of the samples.

temperature, the monoclinic VO₂ (020) reflection is centered at $2\theta = 39.962^{\circ}$ and yields a lattice parameter of 0.2254 nm. The VO₂ reflection suddenly shifts to a lower angle ($2\theta = 39.924^{\circ}$) above this temperature and yields an expanded lattice parameter of 0.2256 nm. These results are consistent with the bulk VO₂ x-ray powder diffraction files (PDFs) for the monoclinic (01-082-0661) and rutile (03-065-9786) phases from the International Centre for Diffraction Data (ICDD) and published XRD studies in the literature³⁶. The shift in the bulk VO₂ structures amounts to $\Delta 2\theta = 0.09^{\circ}$, whereas only a shift of $\Delta 2\theta = 0.04^{\circ}$ is observed in the multilayered thin-film device. This result is not surprising because the VO₂ thin film is under stress due to the mismatch with the sapphire substrate and the Al:ZnO film on top of it. Furthermore, thermal expansion of the VO₂ lattice cannot account for this shift. First, the peak shift is sudden and not gradual with increasing temperature. Second, the average thermal expansion of VO₂ is $\alpha_{ave} = 5.70 \times 10^{-6} \text{K}^{-1}$ (monoclinic structure) and 13.35 $\times 10^{-6} \text{K}^{-1}$ (rutile structure) at these temperatures³⁷, which can only account for approximately 10% of the exhibited lattice expansion (i.e., α_{ave} must be an order of magnitude larger to be responsible for the observed XRD peak shift in the data).

Electrical transport measurements in the form of electrical resistance versus temperature plots are typically used to study the SMT of VO₂ films. In this case, the VO₂ thin film in the Al:ZnO/VO₂ multilayered thin-film device is buried beneath the Al:ZnO film and therefore cannot be measured directly. However, the electrical resistance of the Al:ZnO film can be measured versus temperature using a four-point probe in the van der Pauw configuration and a resistive heater below the sapphire substrate. Figure 7 shows that indeed the SMT of the underlying VO₂ thin film is manifest in the measurements of the Al:ZnO resistance, with the expected hysteresis during heating and cooling. Furthermore, an identical Al:ZnO film without the underlying VO₂ thin film was measured for comparison and shows no hysteretic behavior.

Interestingly, the growth of an Al:ZnO film on top of the VO₂ thin film provides several advantages in addition to placing a thin-film heater in direct contact with the VO₂ (previously mentioned), which creates a mechanism to finely control the heat applied to VO₂ to induce the SMT. The thick Al:ZnO film also acts as a capping layer to the VO₂ thin film and prevents the oxidation of VO₂ to V₂O₅, which is the most energetically favorable and stable phase. Otherwise, over the lifetime of a device, the VO₂ thin film will oxidize to become V₂O₅ and degrade the SMT and its associated properties. Another advantage of the thick Al:ZnO film is its ability to decrease the SMT of the VO₂ thin film by approximately 5–10 °C compared to a VO₂ thin film without an Al:ZnO capping film²⁶. This phenomenon is thought to occur because of the additional stress on the VO₂ thin film and/or an alteration of the oxygen vacancy concentration, which pushes the SMT to a lower temperature³⁸.

In summary, the electro-thermal control of a multilayered Al:ZnO/VO₂ thin-film device was demonstrated, where the SMT of the VO₂ thin film was induced by applying a small potential (<3 V) across the Al:ZnO film. This electro-thermal energy provided by the Al:ZnO film can be finely tuned using its transparent conducting oxide properties to significantly control the VO₂ SMT and its associated electrical and optical properties. The Al:ZnO film acts as a transparent window and heater, serves as a protective capping layer to the VO₂ thin film, and aids in decreasing the VO₂ transition temperature. These results have important implications for the use of VO₂ and Al:ZnO in technological applications, particularly active smart devices.

Methods

Fabrication of the samples. Epitaxial vanadium dioxide (VO₂) thin films were grown on non-annealed c-plane (0001) sapphire (Al₂O₃) substrates (10 mm × 10 mm) at 550 °C using a Neocera pulsed laser deposition (PLD) system, which operated at a base pressure of 10^{-8} Torr. The V₂O₅ target material was ablated with a KrF excimer laser ($\lambda = 248$ nm, pulse width = 25 ns, energy = 220 mJ/pulse) at an angle of 45° with a repetition rate of 3 Hz and a spot size of approximately 2 mm × 4 mm, which resulted in an energy density of ~3 J cm⁻². These 50 nm thick VO₂ thin films, confirmed with x-ray reflectivity and cross-sectional scanning electron microscopy (SEM) [Fig. 1(d)], were grown at a rate of ~0.4 nm/min under a 150 W oxygen (O₂) radio frequency plasma using



Figure 4. Temperature distribution across the sample images in Fig. 2, which are taken along the diagonal between the electrodes from the upper left to the lower right (12.8 mm in length). A fairly uniform temperature distribution (±1 °C below 3 V) is found for the applied voltages studied: (**a**) 1.0 V, (**b**) 1.9 V, (**c**) 2.7 V, (**d**) 3.7 V, (**e**) 4.2 V, and (**f**) 4.7 V.

ultra-high purity (99.994%) O_2 gas at a working pressure of 3.0×10^{-5} Torr (i.e., plasma-assisted PLD). Further details on these VO₂ thin films are discussed elsewhere²⁶.

Aluminum-doped zinc oxide (Al:ZnO) thin films were grown on top of the epitaxial VO₂ (020)_m thin films using a Cambridge NanoTech Ultratech atomic layer deposition (ALD) system, which operated at a base pressure in the mid- 10^{-3} Torr range. Ultra-high purity (99.999%) nitrogen (N₂) gas, which constantly flowed at 20 sccm, was used to purge the chamber and as the carrier gas for the precursors: diethyl zinc [DEZ, Zn(C₂H₅)₂], water (H₂O), and trimethylaluminum [TMA, Al(CH₃)₃]. The Al:ZnO films were grown at 250 °C by alternating between 15 ms pulses of DEZ and H₂O (with 5 s purges in between each) for 20 times (cycles) to obtain a zinc oxide film. After the 20th DEZ pulse, a 15 ms pulse of TMA was used for the aluminum dopant, which was followed by the 20th H₂O pulse. This 20 cycle sequence defines a 1:20 Al:Zn ratio and consequently constitutes the Al:ZnO film used herein. In total, 3000 cycles were performed, which yielded a growth rate of ~0.12 nm per cycle and thus a total thickness of 370 nm [Fig. 1(d)]. Further details on these ALD pulsing sequences and Al:ZnO thin films are discussed elsewhere^{30,31}.

Fabrication of the electrical contacts. $45 \text{ nm gold (Au)}/10 \text{ nm titanium (Ti) electrical contacts (1 mm × 10 mm) were deposited on top of the Al:ZnO/VO₂ multilayered thin film on opposing sides of the$



Figure 5. The 196 cm⁻¹ and 224 cm⁻¹ Raman modes of the VO₂ monoclinic phase were tracked with respect to the temperature by applying voltage to the Al:ZnO/VO₂ multilayered thin-film samples. An SMT temperature between 42 and 46 °C was found, where the red dashed line is a guide to the eye, which indicates a T^{-5} power law dependence. (Inset) Raman spectra of the multilayered thin-film samples below (red line) and above the SMT (blue line), which show that only the modes of the monoclinic semiconducting phase are present below the SMT.



Figure 6. Symmetric θ -2 θ XRD scans of the Al:ZnO/VO₂ multilayered thin-film device at various applied voltages/temperatures. The VO₂ (020)_m reflection suddenly shifts to a lower angle after the SMT is crossed at 43.5 °C.

 $10 \text{ mm} \times 10 \text{ mm}$ sample [Fig. 1(c)]. These contacts were deposited at RT with a growth rate of ~0.12 nm/sec in an AJA International electron beam evaporation system, which operated at a base pressure of 10^{-8} Torr.

SEM imaging techniques. A Hitachi SU8010 field-emission scanning electron microscope (FE-SEM) was used to obtain cross-sectional images of the multilayered $Al:ZnO/VO_2$ thin-film device after cleaving it in half.

Raman spectroscopy characterization techniques. A Horiba LabRAM HR Evolution Raman spectrometer with a laser excitation wavelength ($\lambda = 785$ nm) in the near-infrared range was used because it has been shown to produce the clearest spectrum of Raman active modes for VO₂ on sapphire compared to shorter wavelengths in the visible range³⁵. This clearer spectrum is achieved because resonance fluorescence at these shorter visible wavelengths increases the background and overwhelms the signal from the VO₂ thin film. The spectra were acquired for the Al:ZnO/VO₂ thin-film device at various steady-state temperatures by applying a voltage across the electrical contacts on the Al:ZnO film and using an external resistive heater below the sapphire substrate to increase the temperature.

XRD characterization techniques. A standard high-resolution (0.0001°) four-circle x-ray diffractometer (XRD) with Cu K_{α} radiation and thin-film optics was used to characterize the microstructure of the Al:ZnO/VO₂



Figure 7. Resistance versus temperature plots obtained from four-point probe electrical (I–V) measurements in the van der Pauw configuration. The Al:ZnO/VO₂ multilayered thin-film device shows the expected hysteresis of the VO₂ SMT during heating (red) and cooling (blue). Measurements for an identical Al:ZnO film on sapphire without the VO₂ thin film shows no hysteresis (green).

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thin-film device. Diffraction scans were acquired for the $Al:ZnO/VO_2$ thin-film device at various steady-state temperatures by applying a voltage across the electrical contacts on the Al:ZnO film and using an external resistive heater below the sapphire substrate to increase the temperature.

Electrical transport characterization techniques. The temperature dependence of the electrical resistance of the multilayered $Al:ZnO/VO_2$ thin-film device was measured using a four-point probe in the van der Pauw configuration, where the SMT of the VO_2 was thermally induced with a resistive heater below the sapphire substrate. The four, 50 μ m diameter probes contacted the Al:ZnO film surface, where current-voltage (I–V) measurements were acquired versus temperature using a Keithley 6220 Current Source applying 21 mA of direct current along one edge of the sample and a Keithley 2182A Nanovoltmeter measuring the potential across the other edge.

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Author Contributions

J.R.S., R.M.M. and A.K.P. conceived the ideas and experiments. J.R.S., D.W.S. and R.M.M. conducted the experiments. J.R.S. and D.W.S. analyzed the data and all authors participated in the discussion of the results. J.R.S. wrote the manuscript and all authors reviewed and revised the manuscript.

Additional Information

Competing financial interests: The authors declare no competing financial interests.

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