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# High-mobility junction field-effect transistor via graphene/MoS<sub>2</sub> heterointerface

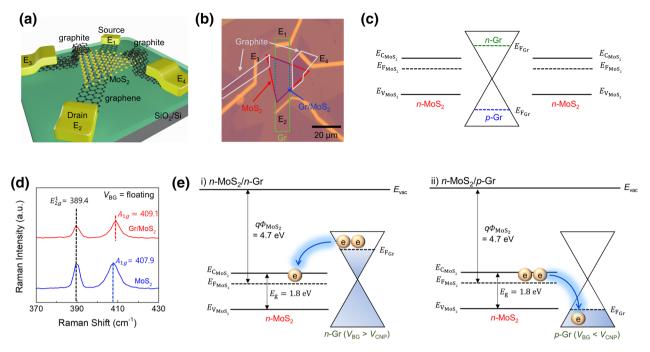
Taesoo Kim<sup>1,2,5</sup>, Sidi Fan<sup>1,2,5</sup>, Sanghyub Lee<sup>1,2</sup>, Min-Kyu Joo<sup>3,4</sup> & Young Hee Lee<sup>1,2</sup>

Monolayer molybdenum disulfide (MoS<sub>2</sub>) possesses a desirable direct bandgap with moderate carrier mobility, whereas graphene (Gr) exhibits a zero bandgap and excellent carrier mobility. Numerous approaches have been suggested for concomitantly realizing high on/off current ratio and high carrier mobility in field-effect transistors, but little is known to date about the effect of two-dimensional layered materials. Herein, we propose a Gr/MoS<sub>2</sub> heterojunction platform, i.e., junction field-effect transistor (JFET), that enhances the carrier mobility by a factor of ~ 10 (~ 100 cm<sup>2</sup> V<sup>-1</sup> s<sup>-1</sup>) compared to that of monolayer MoS<sub>2</sub>, while retaining a high on/off current ratio of ~ 10<sup>8</sup> at room temperature. The Fermi level of Gr can be tuned by the wide back-gate bias ( $V_{BG}$ ) to modulate the effective Schottky barrier height (SBH) at the Gr/MoS<sub>2</sub> heterointerface from 528 meV (*n*-MoS<sub>2</sub>/*p*-Gr) to 116 meV (*n*-MoS<sub>2</sub>/*n*-Gr), consequently enhancing the carrier mobility. The double humps in the transconductance derivative profile clearly reveal the carrier transport mechanism of Gr/MoS<sub>2</sub>, where the barrier height is controlled by electrostatic doping.

Graphene (Gr), which consists of carbon atoms in a planar two-dimensional (2D) array, provides a platform for a new era of 2D electronics to replace mainstream silicon-driven semiconductors owing to its excellent mobility of up to 200,000 cm<sup>2</sup> V<sup>-1</sup> s<sup>-1</sup> at room temperature<sup>1,2</sup>. Yet, the poor switching due to the zero bandgap of Gr remains a critical issue hindering practical applications, despite extensive efforts such as functionalization<sup>3-5</sup>, chemical doping<sup>6</sup>, graphene nanoribbon formation<sup>7-10</sup>, and the development of bilayer graphene with a dual-gate structure<sup>11-13</sup>. Hexagonal molybdenum disulfide (MoS<sub>2</sub>) comprising one molybdenum atom with two surrounding sulfur atoms exhibits *n*-type semiconducting behavior with a high on/off current ratio exceeding 10<sup>8</sup> [ref. <sup>14,16</sup>]. Nevertheless, the moderate carrier mobility (0.1 to 10 cm<sup>2</sup> V<sup>-1</sup> s<sup>-1</sup>)<sup>14,17-19</sup>, is several orders of magnitude lower than that of Gr, thereby limiting the potential of MoS<sub>2</sub> for high-speed switching device applications.

Thus far, numerous approaches have been introduced to enhance electrical performance of  $MoS_2$  based device, for example, gate dielectric and contact resistance engineering have been suggested for enhancing the carrier mobility of  $MoS_2$  to achieve high switching performance. High- $\kappa$  (*e.g.*,  $Al_2O_3$ , HfO<sub>2</sub>) dielectrics suppress the surface reaction and enhance the dielectric screening effect<sup>20-22</sup>, thereby enhancing the carrier mobility of  $MoS_2$  (reaching 81 cm<sup>2</sup> V<sup>-1</sup> s<sup>-1</sup>)<sup>23</sup>. Sub-stoichiometric high- $\kappa$  dielectrics generate more carriers in the thin  $MoS_2$  layer, effectively screening out various Coulomb scattering sources derived from polymer residues, charged impurities, and interface states<sup>15,24</sup>. Encapsulating *h*-BN is a prospectively ideal approach<sup>25,</sup> but mechanical exfoliation is impractical because of the scalability of the cleaved film. Contact resistance engineering by utilizing a low work-function metal such as titanium<sup>26</sup> or scandium<sup>27</sup> is one alternative approach for achieving high carrier mobility. Despite lowering the Schottky barrier height and improving the carrier injection of multilayer  $MoS_2$  significantly, the choice of contact metals for the monolayer in this approach is very limited due to Fermi level pinning depending on the surface states or defect sites of the metal/ $MoS_2$  interface. Recently, it has been successfully demonstrated that monolayer Gr enables to suppress the Schottky barrier height sufficiently at Gr/ $MoS_2$  heterointerface as an ideal contact material for 2D electronic materials<sup>28</sup>, which facilitates diverse

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**Figure 1.** Device structure and Raman characterization of the Gr/MoS<sub>2</sub> heterostructure. (a) Schematic of a two-terminal device with the Gr/MoS<sub>2</sub> heterostructure with graphite as the electrical contact. (b) Optical microscopic image of the heterostructure device. Electrode  $E_1$  and  $E_2$  are the source/drain contacts for the bottom Gr (green rectangular) layer, and  $E_3$  and  $E_4$  are electrically connected to the upper MoS<sub>2</sub> layer via the graphite electrode. (c) Simplified band diagram of the Gr/MoS<sub>2</sub> heterostructure in the lateral direction with graphite contacts ( $E_3$  and  $E_4$ ). (d) Optical Raman spectra of MoS<sub>2</sub> and the Gr/MoS<sub>2</sub> heterostructure. Blue-shift of  $A_{1g}$  peak indicates charge transfer from MoS<sub>2</sub> to Gr. (e) Band diagram of charge transfer direction at the Gr/MoS<sub>2</sub> heterointerface under different gate bias conditions.  $E_{vac}$ ,  $q\Phi_{MoS_2}$ ,  $E_{C_{MoS_2}}$ ,  $E_{V_{MoS_2}}$ ,  $E_g$ , and  $E_{FGr}$  indicate the vacuum level, work function of MoS<sub>2</sub>, conduction band of MoS<sub>2</sub>. Fermi level of MoS<sub>2</sub>, valence band of MoS<sub>2</sub>, band-gap, and Fermi level of Gr, respectively.

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2D heterostructures<sup>29</sup>. Nevertheless, the limited carrier mobility of 2D materials still constrains their practical electronic applications, requiring different approaches.

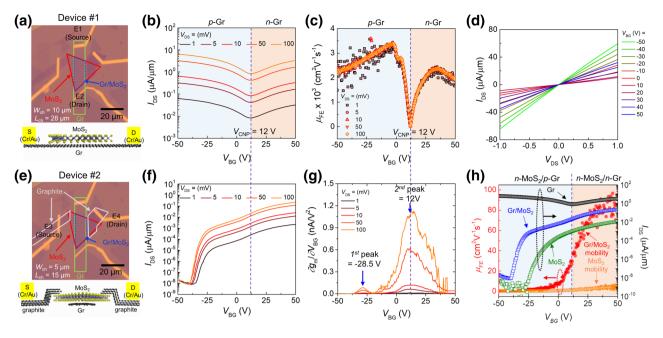
We propose an ideal device platform based on a junction field-effect transistor (JFET) architecture featuring a Gr/MoS<sub>2</sub> heterointerface, where the carrier mobility of MoS<sub>2</sub> is enhanced by a factor of 10, while maintaining a high on/off current ratio of up to 10<sup>8</sup> at room temperature. The Schottky barrier height (SBH) governs the carrier injection and carrier mobility in the Gr/MoS<sub>2</sub> heterojunction device. The low SBH regime at *n*-Gr/*n*-MoS<sub>2</sub> provides an additional Gr conduction path for MoS<sub>2</sub>, leading to  $\mu_{FE} \sim 100 \text{ cm}^2 \text{ V}^{-1} \text{ s}^{-1}$ , whereas the high SBH regime at *p*-Gr/*n*-MoS<sub>2</sub> blocks the contribution of Gr to MoS<sub>2</sub>, leading to  $\mu_{FE} \sim 10 \text{ cm}^2 \text{ V}^{-1} \text{ s}^{-1}$ , similar to that of the pure MoS<sub>2</sub>-based device.

#### **Results and discussions**

To simultaneously achieve high carrier mobility and on/off current ratio, chemical vapor deposited (CVD) monolayer  $MoS_2$  was intentionally stacked on top of a monolayer Gr strip that was also grown by CVD. Properly chosen mechanically exfoliated graphite flakes were employed as the source and drain contacts to protect the  $MoS_2$  channel during metal deposition, thereby circumventing the Fermi level pinning effect<sup>30</sup>. Fig. 1 presents a schematic of the conceptual Gr/MoS<sub>2</sub> heterostructure device with the graphite contact (Fig. 1a), the corresponding optical image (Fig. 1b), and the simplified band diagram (Fig. 1c) in the lateral direction with graphite contacts ( $E_3$  and  $E_4$ ), respectively. The detailed device fabrication procedure is described in the Methods section.

The optical Raman spectrum of the Gr/MoS<sub>2</sub> heterostructure (Fig. 1d) was acquired under argon atmosphere to prevent unintended oxidation or degradation. The  $E_{2g}^1$  peaks (~ 389.4 cm<sup>-1</sup>) of bare MoS<sub>2</sub> and the Gr/MoS<sub>2</sub> heterostructure are similarly positioned, implying negligible strain in the developed device. The blue-shift of the  $A_{1g}$  peak for the heterostructure with respect to that of MoS<sub>2</sub> indicates electron charge transfer from MoS<sub>2</sub> to Gr, as reported previously<sup>31,32</sup>. The relevant band diagram depending on the carrier type of Gr (Fig. 1e) illustrates the vertical charge transfer direction based on the gate bias. In the *n*-Gr/*n*-MoS<sub>2</sub> regime, the electron population in *n*-Gr is transferred to *n*-MoS<sub>2</sub>, leading to a negligible vertical SBH, whereas in the *p*-Gr/*n*-MoS<sub>2</sub> regime, electrons in *n*-MoS<sub>2</sub> are transferred to *p*-Gr, resulting in a large vertical SBH, as discussed later. This charge transfer model explains the key carrier transport mechanism in the developed heterostructure device associated with the vertical SBH at the Gr/MoS<sub>2</sub> heterojunction.

We first examined the back-gate bias ( $V_{BG}$ )-dependent drain current ( $I_{DS}$ ) of graphene (Device #1: Gr-Gr/MoS<sub>2</sub>-Gr using metal electrodes  $E_1$  and  $E_2$ ) in the Gr/MoS<sub>2</sub> heterostructure as displayed in Fig. 2a. The transfer curves based on the drain-source voltage ( $V_{DS}$ ) (Fig. 2b) demonstrate the ambipolar characteristics of the Gr in



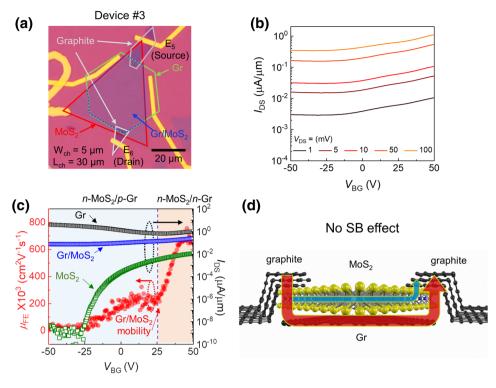
**Figure 2.** Electrical characteristics of Gr/MoS<sub>2</sub> heterostructure device. (**a**–**d**) Gr/MoS<sub>2</sub> heterostructure device employing Gr contact with E<sub>1</sub> (source) and E<sub>2</sub> (drain) (Device #1: Gr-Gr/MoS<sub>2</sub>-Gr, *L*/*W*=28 µm/10 µm). (**a**) Optical device image,  $V_{DS}$ -dependent (**b**)  $I_{DS}-V_{BG}$  transfer curves ( $V_{CNP}$ =12 V) and (**c**)  $\mu_{FE}$ , respectively. (**d**)  $V_{BG}$ -dependent  $I_{DS}-V_{DS}$  output characteristic curves. (**e**–**g**) Gr/MoS<sub>2</sub> heterostructure device employing MoS<sub>2</sub> contact with E<sub>3</sub> and E<sub>4</sub> (Device #2: graphite-MoS<sub>2</sub>-Gr/MoS<sub>2</sub>-MoS<sub>2</sub>-graphite, *L*/*W*=15 µm/5 µm). (**e**) Optical device image,  $V_{DS}$ -dependent (**f**) transfer curves and (**g**) corresponding derivative of  $g_m$  (= $\partial g_m/\partial V_{BG}$ ) curves. The 1st and 2nd peak positions of  $V_{BG}$  indicate the  $V_{FB}$  of MoS<sub>2</sub> and  $V_{CNP}$  of Gr. (**h**) Comparison of transfer curves of the three devices (Gr, MoS<sub>2</sub>, Device #2) (right *y*-axis) and the  $\mu_{FE}$  (left *y*-axis) of MoS<sub>2</sub> and Device #2 at  $V_{DS}$ =0.1 V. An enhancement of  $\mu_{FE}$  (~100 cm<sup>2</sup> V<sup>-1</sup> s<sup>-1</sup>) is clearly observed in the *n*-MoS<sub>2</sub>/*n*-Gr regime.

Device #1. The charge neutrality point ( $V_{\rm CNP}$ ) is found at  $V_{\rm BG}$ =12 V regardless of  $V_{\rm DS}$ . This positive  $V_{\rm CNP}$  indicates dominant electron transfer from Gr to the positive fixed oxide traps in the SiO<sub>2</sub>/Si substrate<sup>31,33,34</sup>. The field-effect mobility is described as  $\mu_{\rm FE} = g_{\rm m} (L/W) C_{\rm OX}^{-1} V_{\rm DS}^{-1}$ , where  $g_{\rm m}$ , L/W, and  $C_{\rm OX}$  denote the transconductance (= $\partial I_{\rm DS}/\partial V_{\rm BG}$ ), channel length-to-width ratio, and oxide capacitance per unit area, respectively. The maximum  $\mu_{\rm FE}$  is ~2,500 cm<sup>2</sup> V<sup>-1</sup> s<sup>-1</sup> for *n*-Gr and ~3,500 cm<sup>2</sup> V<sup>-1</sup> s<sup>-1</sup> for *p*-Gr (Fig. 2c). The  $\mu_{\rm FE}$  range is similar to that of the bare Gr device on SiO<sub>2</sub><sup>35</sup>. The collapse of the  $V_{\rm DS}$ -dependent  $\mu_{\rm FE}$  clearly mirrors the negligible SBH effect. Further, super-linear  $I_{\rm DS} - V_{\rm DS}$  output characteristic curves were obtained because of the high tunability of the Fermi-level of Gr (Fig. 2d), confirming the low SBH or nearly ohmic contact.

Investigation of the Gr/MoS<sub>2</sub> heterostructure device with the MoS<sub>2</sub> contact in the two graphite electrodes (Device #2: graphite-MoS<sub>2</sub>-Gr/MoS<sub>2</sub>-graphite using metal electrodes E<sub>3</sub> and E<sub>4</sub>) (see Fig. 2e), shows a distinct current path difference comparing to Device #1. Because the conductivity of MoS<sub>2</sub> is much lower than that of Gr or the Gr/MoS<sub>2</sub> heterojunction in this case, the bare MoS<sub>2</sub> dominates the carrier transport of Device #2, in contrast with Device #1. The  $I_{DS}-V_{BG}$  transfer curves (Fig. 2f) show clear switching behavior in the negative  $V_{BG}$  regime, originating primarily from the bare MoS<sub>2</sub> component in Device #2. Notably, in the positive  $V_{BG}$  regime, a hump associated with a rapid enhancement of  $I_{DS}$  appears, independent of  $V_{DS}$ . This strongly implies the presence of two different conduction mechanisms or conducting paths, similar to double-gate Si and junctionless Si transistors<sup>36,37</sup>.

To gain insight into the carrier transport mechanism of Device #2 in detail, the flat-band voltage ( $V_{\text{FB}}$ ) was determined from the second-derivative of the current (Fig. 2g)<sup>38</sup>. The first peak at  $V_{\text{BG}} = -28.5$  V is attributed to the turn-on voltage of MoS<sub>2</sub>. The second peak at  $V_{\text{BG}} = 12$  V is ascribed to the underlying Gr in Device #2, as confirmed by the identical  $V_{\text{CNP}}$  of Gr (Fig. 2c). The coincident position of the second hump for  $V_{\text{FB}}$  and  $V_{\text{CNP}}$  further rationalizes the presence of second conducting path Device #2. Since it has theoretically and experimentally been suggested that the vertical SBH of Gr/MoS<sub>2</sub> can be modulated by electrostatic doping<sup>39–42</sup>, the bare MoS<sub>2</sub> consequently governs the carrier transport of the device exclusively when  $V_{\text{BG}} \leq V_{\text{CNP}}$  (*i.e.*, *p*-Gr/*n*-MoS<sub>2</sub>) at high vertical SBH. Another current path involving Gr in addition to MoS<sub>2</sub> is provided at  $V_{\text{BG}} \geq V_{\text{CNP}}$  (*i.e.*, *n*-Gr/*n*-MoS<sub>2</sub>) with low vertical SBH, leading to  $\mu_{\text{FE}}$  for Device #2.

To have a better picture,  $\mu_{FE}$  of heterostructure (Device #2) and bare MoS<sub>2</sub> (Device #5 with metal electrodes E<sub>9</sub> and E<sub>10</sub>, see Supplementary Note 1 and Fig. S1) at  $V_{DS}$  = 0.1 V is compared together in Fig. 2h. In principle, the current onset voltage for Device #2 and Device #5 should be identical because the bare MoS<sub>2</sub> in both of them limits the carrier transport in the subthreshold regime. We ascribed this mismatched current onset voltage in Fig. 2h between the Gr/MoS<sub>2</sub> (Device #2) and MoS<sub>2</sub> (Device #5) to the different effective SBHs. While the MoS<sub>2</sub> (Device #5) has a Cr/Au contact to MoS<sub>2</sub>, the MoS<sub>2</sub>/Gr (Device #2) possesses a graphite contact to MoS<sub>2</sub>, respectively. The maximum  $\mu_{FE}$  of Device #5 ranges from 8 to 10 cm<sup>2</sup> V<sup>-1</sup> s<sup>-1</sup> at  $V_{BG}$  = 50 V, which reaches to 16–20 cm<sup>2</sup> V<sup>-1</sup> s<sup>-1</sup> after contact resistance correction (see Supplementary Fig. S2). Meanwhile, the  $\mu_{FE}$  of Device #2 is

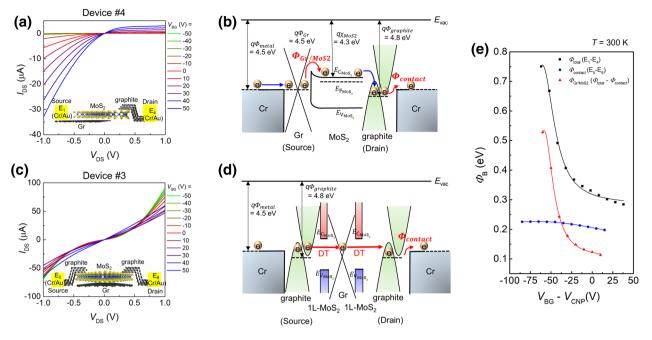


**Figure 3.** Vertical Gr/MoS<sub>2</sub> heterointerface device (the bottom Gr, MoS<sub>2</sub>, and top graphite layers all overlapped). (**a**) Optical image of the vertical Gr/MoS<sub>2</sub> heterointerface device employing graphite contact with  $E_5$  (source) and  $E_6$  (drain) (Device #3: graphite-Gr/MoS<sub>2</sub>-graphite,  $L/W = 30 \ \mu\text{m}/5 \ \mu\text{m}$ ). (**b**)  $V_{\text{DS}}$ -dependent transfer curve shows low on/off current ratio. (**c**) Comparison of transfer curves of the three devices (right y-axis) and  $V_{\text{BG}}$ -dependent  $\mu_{\text{FE}}$  (left y-axis) of Device #3 at  $V_{\text{DS}} = 0.1 \ \text{V}$ . (**d**) Current path marked by arrows for this device structure. Direct tunneling (DT) from graphite to graphene through monolayer MoS<sub>2</sub> is suggested, as indicated by the red arrow. A small portion of the current flow along the MoS<sub>2</sub> layer is represented by the blue arrow.

slowly developed after the turn-on voltage of MoS<sub>2</sub> at  $V_{BG} = -28.5$  V which is very similar to  $\mu_{FE}$  of Device #5. This  $\mu_{FE}$  behavior reflects clearly that the bare MoS<sub>2</sub> part in Device #2 limits the overall carrier transport in this  $V_{BG}$  regime. But,  $\mu_{FE}$  rapidly increases after  $V_{FB}$  or  $V_{CNP}$  reaching  $\mu_{FE}$  saturation (~100 cm<sup>2</sup> V<sup>-1</sup> s<sup>-1</sup>) at  $V_{BG} = 50$  V. This mobility enhancement is mainly ascribed to Gr with low SBH in the central *n*-Gr/*n*-MoS<sub>2</sub> heterojunction regime in Device #2. The high on/off current ratio of the device is further attributed to the bare MoS<sub>2</sub> region in the device. As a consequence, provided that the portion of bare MoS<sub>2</sub> in Device #2 shrinks as short as possible without losing the on/off current ratio, a further  $\mu_{FE}$  improvement would expect via a device layout optimization. All devices were annealed at T = 150 °C for 2 h in high vacuum chamber before the electrical measurement to eliminate effect of adsorbates and interface trap sites between 2D materials and dielectrics (See Supplementary Fig. S3). Hysteresis at Gr/MoS<sub>2</sub> interface was negligible from that of MoS<sub>2</sub> device.

To systematically identify the transport mechanism in the Gr/MoS<sub>2</sub> heterostructure, we constructed a device (Device #3: graphite-Gr/MoS<sub>2</sub>-graphite using metal electrodes  $E_5$  and  $E_6$ ) with the two graphite electrodes overlapping the lower graphene layer, intentionally excluding the bare MoS<sub>2</sub> region (see Fig. 3a). It is worthy to underline that device structure of Device #3 is in contrast with that of Device #2 in particular for a spatial distance between graphite and underlying Gr (see Fig. 2e and Fig. 3d). The  $V_{DS}$ -dependent transfer curves (Fig. 3b) were obtained from the two graphite electrodes ( $E_5$  and  $E_6$ ). The lack of a high on/off current ratio is ascribed to the direct tunneling (DT) current across the atomically thin monolayer  $MoS_2$  to the underlying graphene. The screening effect of the underlying graphene is another plausible underlying factor, resulting in weak gate modulation in the MoS<sub>2</sub> channel. From the transfer curves of individual Gr, MoS<sub>2</sub>, and the Gr/MoS<sub>2</sub> heterostructure (Fig. 3c), the charge neutrality point of Gr was found at  $V_{BG} = 27$  V. General  $\mu_{FE}$  behavior of Device #3 is very similar to that of Device #1 as shown in Fig. 2h. The high  $\mu_{\text{FE}}$  (~800 cm<sup>2</sup> V<sup>-1</sup> s<sup>-1</sup>) of Device #3 in the  $V_{\text{BG}} \ge V_{\text{CNP}}$ confirms clearly that the origin of high  $\mu_{\rm FE}$  is the underlying Gr. Further relevant electrical characteristics of Gr in Device #3 is discussed in Supplementary Fig. S4. In such a structure, even if the SBH at the  $Gr/MoS_2$  interface is modulated by  $V_{\rm BG}$ , the Schottky barrier effect is largely suppressed by direct tunneling at the interlayer distance, while a small portion of the total current flows along the MoS<sub>2</sub> channel based on the conductivity ratio between Gr and MoS<sub>2</sub> (Fig. 3d).

Now, we turn to evaluate the  $V_{BG}$ -dependent vertical SBH in the Gr/MoS<sub>2</sub> heterojunction at a given  $V_{BG}$  using Richardson's equation<sup>43</sup>:



**Figure 4.** Determination of Schottky barrier height at Gr/MoS<sub>2</sub> heterointerface and band diagram. (a)  $V_{BG}$ -dependent output characteristic curves of Gr/MoS<sub>2</sub> heterostructure device contact with E<sub>1</sub> and E<sub>4</sub> (Device #4: Gr-Gr/MoS<sub>2</sub>-graphite) and (b) corresponding band diagram. (c)  $V_{BG}$ -dependent output characteristic curves of Device #3 and (d) corresponding band diagram. (e) Effective SBH for Gr/MoS<sub>2</sub> heterointerface ( $\Phi_{Gr/MoS2}$ ) as a function of  $V_{BG}$ - $V_{CNP}$ 

$$I_{\rm DS} = I_0 \exp(q V_{\rm DS}/k_{\rm B}T) \times \left[1 - \exp\left(-\frac{q V_{\rm DS}}{k_{\rm B}T}\right)\right]$$
(1)

where  $I_0 = AA^*T^{3/2}\exp(-\frac{q\Phi_B}{k_BT})$ , *A* is the junction area,  $A^*$  (= 54 A K<sup>-2</sup> cm<sup>-2</sup>) is the Richardson constant<sup>44</sup>, *n* is the ideality factor,  $k_B$  is the Boltzmann constant, *q* is the electrical unit charge,  $\Phi_B$  is the effective SBH, and *T* is the absolute temperature. Rearranging Eq. 1 gives:

$$\ln\left[\frac{I_{DS}exp(qV_{DS}/k_{B}T)}{exp(qV_{DS}/k_{B}T)-1}\right] = \ln(I_{0}) + \frac{qV_{DS}}{nk_{B}T}$$
(2)

where *n* and  $\Phi_{\rm B}$  can be respectively determined from the slope and *y*-intercept of the  $\ln[I_{\rm DS} \times \exp(qV_{\rm DS}/k_{\rm B}T)/(\exp(qV_{\rm DS}/k_{\rm B}T) - 1)]$  versus the negative  $V_{\rm DS}$  plot, (see Supplementary Note 2 and Fig. S5–S6 for the detailed procedures for SBH estimation).

The  $V_{BG}$ -dependent  $I_{DS}$ - $V_{DS}$  curves obtained from Device #4 (Gr-Gr/MoS<sub>2</sub>-graphite using metal electrodes  $E_1$  and  $E_4$ ) are plotted in Fig. 4a. The asymmetric  $I_{DS} - V_{DS}$  curve is mainly derived from the asymmetric contact barrier (graphene contact vs. graphite contact). The band alignment is illustrated, along with the two dominating Schottky barriers that contribute to the total SBH (Fig. 4b). One barrier is the SB at the Gr/MoS<sub>2</sub> heterojunction ( $\Phi_{Gr/MoS_1}$ ) and the other is the SB at the graphite/Cr contact junction ( $\Phi_{contact}$ ). At the source terminal, the contact resistance at the Gr/Cr junction is negligible due to the low SB or nearly ohmic contact<sup>42</sup>, which is consistent with the linear output characteristics illustrated in Fig. 2d. The SB was clearly formed at the Gr/MoS<sub>2</sub> junction due to the difference in the work function of the two materials. Thereafter, electrons flow naturally from MoS<sub>2</sub> to graphite because the energy state of the conduction band of MoS<sub>2</sub> is higher (~4.3 eV) than that of graphite ( $\sim 4.8 \text{ eV}$ )<sup>30</sup>. This implies that the SB at the MoS<sub>2</sub>/graphite interface can be excluded from the total SBH estimation. Electrons are then thermally emitted to Cr by overcoming the SB at the graphite/Cr interface, where the contact barrier is formed. Therefore, the total SBH ( $\Phi_{total}$ ) obtained from this structure is attributed to the SB at the Gr/MoS<sub>2</sub> ( $\Phi_{Gr/MoS_2}$ ) heterojunction and graphite/Cr contact ( $\Phi_{contact}$ ), where the total SBH can be expressed as:  $\Phi_{\text{total}} = \Phi_{\text{Gr/MoS}_2} + \Phi_{\text{contact}}$ . The  $I_{\text{DS}} - V_{\text{DS}}$  curves of Device #3 present non-linear behavior in the  $V_{\text{BG}}$  regimes below  $V_{\text{CNP}}$  (-50 V  $\leq V_{\text{BG}} \leq 20$  V) (see Fig. 4c). The low on/off current ratio of this device (see Fig. 3b) provides clear evidence of DT, where the electrons flow through the atomically thin monolayer  $MoS_2$  to the underlying Gr, leading to a negligible contribution of Gr/MoS<sub>2</sub> SB to the total SBH. Accordingly,  $\Phi_{contact}$  at the graphite/Cr interface could be regarded as  $\Phi_{total}$  for this device structure, where  $\Phi_{total} \simeq \Phi_{contact}$  (see Fig. 4d). Consequently,  $\Phi_{Gr/MoS}$ , can be deduced by subtracting  $\Phi_{contact}$  from  $\Phi_{total}$ . The  $V_{BG}$ -dependent  $\Phi_{total}$ ,  $\Phi_{contact}$ , and effective  $\Phi_{Gr/MoS}$ , at T = 300 K are directly compared in Fig. 4e. Increasing  $V_{BG}$  reduced  $\Phi_{Gr/MoS}$ , when the value of  $V_{BG}$  was positive in the range of 528–116 meV, clearly indicating the large Fermi-level tunability of Gr via electrostatic gating.

#### Conclusion

In conclusion, we propose that the Gr/MoS<sub>2</sub> heterointerface can be employed as a high-performance electronic device. The high carrier mobility of 100 cm<sup>2</sup> V<sup>-1</sup> s<sup>-1</sup> of Gr/MoS<sub>2</sub> heterojunction device over 8–10 cm<sup>2</sup> V<sup>-1</sup> s<sup>-1</sup> of MoS<sub>2</sub> device is ascribed to the underlying Gr, which is activated when the low SBH is formed in the *n*-Gr/*n*-MoS<sub>2</sub> regime. On the other hand, the high on/off current ratio of ~ 10<sup>8</sup> is attributed to the bare MoS<sub>2</sub> region in the heterostructure device. Furthermore, we demonstrate that the high tunability of the Fermi level of Gr allows to control the SBH at the Gr/MoS<sub>2</sub> interface, resulting in distinctive carrier conduction features of the Gr/MoS<sub>2</sub> heterojunction device, and confirming consequently two different conduction mechanisms.

#### Methods

**Material synthesis.** Monolayer Gr flakes were first synthesized on a 100  $\mu$ m thick Cu foil (111) in a chemical vapor deposition (CVD) chamber under hydrogen with a low concentration of methane (0.1%, balance Ar gas) as a carbon source. The synthesized graphene with the polymer supporting layer was softly detached by bubble interception in sodium hydroxide solution and rinsed thrice with distilled water to eliminate contaminants during the chemical wet etching process. The graphene layer was then transferred onto a 300 nm-thick SiO<sub>2</sub> substrate. Reactive ion etching (RIE, AFS-4RT; O<sub>2</sub> plasma power of 30 mW; flow rate of 20 sccm at 10<sup>-3</sup> torr) was used to define the channel area of Gr. Monolayer MoS<sub>2</sub> flakes were grown in a separate CVD chamber and transferred onto graphene strips by a water-assisted transfer method<sup>45</sup>. Thereafter, the exfoliated graphite flakes (~ 5 nm) were placed on the Gr/MoS<sub>2</sub> heterostructure by the aligned transfer technique, in a glove box. Finally, the source/drain metal electrodes were completed within 24 h to maintain consistency in terms of the device reliability and reproducibility.

**Device fabrication.** The devices were fabricated by a series of wet- and dry-cooperative transfer methods. The channel materials (single-crystal graphene and MoS<sub>2</sub>) were both prepared by CVD<sup>35,46</sup>. The graphene synthesized on copper was firstly delaminated by electrochemical bubbling and then transferred onto the target 300 nm SiO<sub>2</sub>/Si substrates<sup>47</sup>. Thereafter, the MoS<sub>2</sub> grown on the SiO<sub>2</sub>/Si substrate was detached by chemical etching<sup>48</sup>. After rinsing copiously with deionized water, the MoS<sub>2</sub> flakes carried by the polymethyl methacrylate (PMMA) transfer film were assembled on the dry transfer holder. The two channels were aligned in the desired positions, and MoS<sub>2</sub> was brought into contact with the graphene and held at 140 °C for 5 min, thus isolating the PMMA film from the transfer holder. The electrical contact of MoS<sub>2</sub> was improved by using graphite, which was mechanically exfoliated onto the PMMA coated 300 nm SiO<sub>2</sub>/Si substrates and then deposited on the designed positions inside or outside the overlapped graphene/MoS<sub>2</sub> by the conventional PMMA supporting layer method<sup>49</sup>. Subsequently, the source and drain for the graphene channel and the contact to the graphite electrodes were patterned by e-beam lithography, followed by vapor deposition of 5/50 nm Cr/Au. For the strip graphene channel, an additional patterning step was performed by photolithography prior to O<sub>2</sub>-plasma dry etching.

**Optical and electrical characterization.** The optical Raman spectrum of the developed Gr/MoS<sub>2</sub> heterointerface device was acquired under 532 nm laser excitation with a power of 12  $\mu$ W (Witec Alpha 300) in an argon-filled glove box system, where the oxygen and moisture levels were kept below 1 ppm to prevent undesired oxidation and aging of the samples. Electrical measurements were carried out using a commercial source measurement unit (Keithley 4200-SCS) under low-vacuum conditions (~10<sup>-3</sup> torr) in a vacuum probe station, at room temperature.

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#### Author contributions

T.K., S.F., Y.H.L., and M.-K.J. conceived the research and designed the experiment. T.K. and S.F. performed most of the experiments including device fabrication, characterization and data analysis. S.L. prepared the CVD graphene and MoS<sub>2</sub> samples. T.K., S.F., and M.-K.J. illustrated all the images and plots in main figures and supplementary information. T.K., S.F., M.-K.J., and Y.H.L. analyzed the results and wrote the manuscript. All authors discussed the results and commented on the manuscript.

#### **Competing interests**

The authors declare no competing financial interests.

#### Additional information

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