

CORRECTIONS & AMENDMENTS

CORRECTION

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Publisher Correction: Integrating photonics with silicon nanoelectronics for the next generation of systems on a chip

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In this Letter, owing to an error during the production process, the author affiliations were listed incorrectly. Affiliation number 5 (Colleges of Nanoscale Science and Engineering, State University of New York (SUNY)) was repeated, and affiliation numbers 6–8 were incorrect. In addition, the phrase “two oxide thickness variants” should have been “two gate oxide thickness variants”. These errors have all been corrected online.